

Simulink®

Modeling Guidelines for High-Integrity Systems

R2012a

**MATLAB®
& SIMULINK®**

How to Contact MathWorks



www.mathworks.com Web
comp.soft-sys.matlab Newsgroup
www.mathworks.com/contact_TS.html Technical Support



suggest@mathworks.com Product enhancement suggestions
bugs@mathworks.com Bug reports
doc@mathworks.com Documentation error reports
service@mathworks.com Order status, license renewals, passcodes
info@mathworks.com Sales, pricing, and general information



508-647-7000 (Phone)



508-647-7001 (Fax)



The MathWorks, Inc.
3 Apple Hill Drive
Natick, MA 01760-2098

For contact information about worldwide offices, see the MathWorks Web site.

Modeling Guidelines for High-Integrity Systems

© COPYRIGHT 2009–2012 by The MathWorks, Inc.

The software described in this document is furnished under a license agreement. The software may be used or copied only under the terms of the license agreement. No part of this manual may be photocopied or reproduced in any form without prior written consent from The MathWorks, Inc.

FEDERAL ACQUISITION: This provision applies to all acquisitions of the Program and Documentation by, for, or through the federal government of the United States. By accepting delivery of the Program or Documentation, the government hereby agrees that this software or documentation qualifies as commercial computer software or commercial computer software documentation as such terms are used or defined in FAR 12.212, DFARS Part 227.72, and DFARS 252.227-7014. Accordingly, the terms and conditions of this Agreement and only those rights specified in this Agreement, shall pertain to and govern the use, modification, reproduction, release, performance, display, and disclosure of the Program and Documentation by the federal government (or other entity acquiring for or through the federal government) and shall supersede any conflicting contractual terms or conditions. If this License fails to meet the government's needs or is inconsistent in any respect with federal procurement law, the government agrees to return the Program and Documentation, unused, to The MathWorks, Inc.

Trademarks

MATLAB and Simulink are registered trademarks of The MathWorks, Inc. See www.mathworks.com/trademarks for a list of additional trademarks. Other product or brand names may be trademarks or registered trademarks of their respective holders.

Patents

MathWorks products are protected by one or more U.S. patents. Please see www.mathworks.com/patents for more information.

Revision History

September 2009 Online only
April 2010 Online only
September 2010 Online only
April 2011 Online only
September 2011 Online only
March 2012 Online only

New for Version 1.0 (Release 2009b)
Revised for Version 1.1 (Release 2010a)
Revised for Version 1.2 (Release 2010b)
Revised for Version 1.3 (Release 2011a)
Revised for Version 1.4 (Release 2011b)
Revised for Version 1.5 (Release 2012a)

Introduction

1

Motivation	1-2
-------------------------	-----

Block Considerations

2

Math Operations	2-2
hisl_0001: Usage of Abs block	2-3
hisl_0002: Usage of Math Function blocks (remainder and reciprocal)	2-5
hisl_0003: Usage of Square Root blocks	2-7
hisl_0004: Usage of Math Function blocks (natural logarithm and base 10 logarithm)	2-9
hisl_0005: Usage of Product blocks	2-12
Ports & Subsystems	2-14
hisl_0006: Usage of While Iterator blocks	2-15
hisl_0007: Usage of While Iterator subsystems	2-17
hisl_0008: Usage of For Iterator Blocks	2-20
hisl_0009: Usage of For Iterator Subsystem blocks	2-22
hisl_0010: Usage of If blocks and If Action Subsystem blocks	2-23
hisl_0011: Usage of Switch Case blocks and Action Subsystem blocks	2-25
hisl_0012: Usage of conditionally executed subsystems ...	2-27
Signal Routing	2-29
hisl_0013: Usage of data store blocks	2-30
hisl_0015: Usage of Merge blocks	2-33
hisl_0021: Consistent vector indexing method	2-35
hisl_0022: Data type selection for index signals	2-36
hisl_0023: Verification of model and subsystem variants ..	2-37

Logic and Bit Operations	2-38
hisl_0016: Usage of blocks that compute relational operators	2-39
hisl_0017: Usage of blocks that compute relational operators (2)	2-41
hisl_0018: Usage of Logical Operator block	2-42
hisl_0019: Usage of Bitwise Operator block	2-43

Configuration Parameter Considerations

3

Solver	3-2
hisl_0040: Configuration Parameters > Solver > Simulation time	3-3
hisl_0041: Configuration Parameters > Solver > Solver options	3-4
hisl_0042: Configuration Parameters > Solver > Tasking and sample time options	3-5
 Diagnostics	 3-7
hisl_0043: Configuration Parameters > Diagnostics > Solver	3-8
hisl_0044: Configuration Parameters > Diagnostics > Sample Time	3-10
hisl_0301: Configuration Parameters > Diagnostics > Compatibility	3-13
hisl_0302: Configuration Parameters > Diagnostics > Data Validity > Parameters	3-14
hisl_0303: Configuration Parameters > Diagnostics > Data Validity > Merge block	3-15
hisl_0304: Configuration Parameters > Diagnostics > Data Validity > Model Initialization	3-16
hisl_0305: Configuration Parameters > Diagnostics > Data Validity > Debugging	3-17
hisl_0306: Configuration Parameters > Diagnostics > Connectivity > Signals	3-18
hisl_0307: Configuration Parameters > Diagnostics > Connectivity > Buses	3-19
hisl_0308: Configuration Parameters > Diagnostics > Connectivity > Function calls	3-20

hisl_0309: Configuration Parameters > Diagnostics > Type Conversion	3-21
hisl_0310: Configuration Parameters > Diagnostics > Model Referencing	3-22
hisl_0311: Configuration Parameters > Diagnostics > Stateflow	3-23
Optimizations	3-24
hisl_0045: Configuration Parameters > Optimization > Implement logic signals as Boolean data (vs. double) ..	3-25
hisl_0046: Configuration Parameters > Optimization > Block reduction	3-26
hisl_0047: Configuration Parameters > Optimization > Conditional input branch execution	3-27
hisl_0048: Configuration Parameters > Optimization > Application lifespan (days)	3-28
hisl_0051: Configuration Parameters > Optimization > Signals and Parameters > Loop unrolling threshold ...	3-29
hisl_0052: Configuration Parameters > Optimization > Data initialization	3-30
hisl_0053: Configuration Parameters > Optimization > Remove code from floating-point to integer conversions that wraps out-of-range values	3-31
hisl_0054: Configuration Parameters > Optimization > Remove code that protects against division arithmetic exceptions	3-32
hisl_0055: Prioritization of code generation objectives for high-integrity systems	3-33

Stateflow Chart Considerations

4

Chart Properties	4-2
hisf_0001: Mealy and Moore semantics	4-3
hisf_0002: User-specified state/transition execution order	4-5
hisf_0009: Strong data typing (Simulink and Stateflow boundary)	4-7
hisf_0011: Stateflow debugging settings	4-8

Chart Architecture	4-10
hisf_0003: Usage of bitwise operations	4-11
hisf_0004: Usage of recursive behavior	4-12
hisf_0007: Usage of junction conditions (maintaining mutual exclusion)	4-15
hisf_0010: Usage of transition paths (looping out of parent of source and destination objects)	4-16
hisf_0012: Chart comments	4-18
hisf_0013: Usage of transition paths (crossing parallel state boundaries)	4-19
hisf_0014: Usage of transition paths (passing through states)	4-21
hisf_0015: Strong data typing (casting variables and parameters in expressions)	4-22

MISRA-C:2004 Compliance Considerations

5

Modeling Style	5-2
hisl_0061: Unique identifiers for clarity	5-3
hisl_0062: Global variables in graphical functions	5-5
hisl_0063: Length of user-defined function names to improve MISRA-C:2004 compliance	5-8
hisl_0064: Length of user-defined type object names to improve MISRA-C:2004 compliance	5-9
hisl_0065: Length of signal and parameter names to improve MISRA-C:2004 compliance	5-10
hisl_0201: Define reserved keywords to improve MISRA-C:2004 compliance	5-11
hisl_0202: Use of data conversion blocks to improve MISRA-C:2004 compliance	5-12
Block Usage	5-14
hisl_0020: Blocks not recommended for MISRA-C:2004 compliance	5-14
hisl_0101: Avoid invariant comparison operations to improve MISRA-C:2004 compliance	5-15
hisl_0102: Data type of loop control variables to improve MISRA-C:2004 compliance	5-18

Configuration Settings	5-19
hisl_0060: Configuration parameters that improve MISRA-C:2004 compliance	5-19
hisl_0312: Specify target specific configuration parameters to improve MISRA-C:2004 compliance	5-21
hisl_0313: Selection of bitfield data types to improve MISRA-C:2004 compliance	5-23
Stateflow Chart Considerations	5-24
hisf_0064: Shift operations for Stateflow data to improve MISRA-C:2004 compliance	5-25
hisf_0065: Type cast operations in Stateflow to improve MISRA-C:2004 compliance	5-27
hisf_0211: Protect against use of unary operators in Stateflow Charts to improve MISRA-C:2004 compliance	5-29
hisf_0212: Data type of Stateflow for loop control variables to improve MISRA-C: 2004 compliance	5-31
hisf_0213: Protect against divide-by-zero calculations in Stateflow charts to improve MISRA-C: 2004 compliance	5-32
System Level	5-35
hisl_0401: Encapsulation of code to improve MISRA-C:2004 compliance	5-35
hisl_0402: Use of custom #pragma to improve MISRA-C:2004 compliance	5-36
hisl_0403: Use of char data type improve MISRA-C:2004 compliance	5-37

Introduction

Motivation

MathWorks intends this document for engineers developing models and generating code for high-integrity systems using Model-Based Design with MathWorks® products. This document describes creating Simulink® models that are complete, unambiguous, statically deterministic, robust, and verifiable. The document focus is on model settings, block usage, and block parameters that impact simulation behavior or code generated by the Embedded Coder™ product.

These guidelines do not assume that you use a particular safety or certification standard. The guidelines reference some safety standards where applicable, including DO-178B, IEC 61508, ISO 26262, and MISRA C®.

You can use the Model Advisor to support adhering to these guidelines. Each guideline lists the checks that are applicable to that guideline, or to parts of that guideline.

This document does not address model style or development processes. For more information about creating models in a way that improves consistency, clarity, and readability, see the “MathWorks Automotive Advisory Board Control Algorithm Modeling Guidelines Using MATLAB®, Simulink, and Stateflow®”. Development process guidance and additional information for specific standards is available with the IEC Certification Kit (for IEC 61508 and ISO 26262) and DO Qualification Kit (for DO-178B and DO-254) products.

Disclaimer While adhering to the recommendations in this document will reduce the risk that an error is introduced during development and not be detected, it is not a guarantee that the system being developed will be safe. Conversely, if some of the recommendations in this document are not followed, it does not mean that the system being developed will be unsafe.

Block Considerations

- “Math Operations” on page 2-2
- “Ports & Subsystems” on page 2-14
- “Signal Routing” on page 2-29
- “Logic and Bit Operations” on page 2-38

Math Operations

In this section...
“hisl_0001: Usage of Abs block” on page 2-3
“hisl_0002: Usage of Math Function blocks (remainder and reciprocal)” on page 2-5
“hisl_0003: Usage of Square Root blocks” on page 2-7
“hisl_0004: Usage of Math Function blocks (natural logarithm and base 10 logarithm)” on page 2-9
“hisl_0005: Usage of Product blocks” on page 2-12

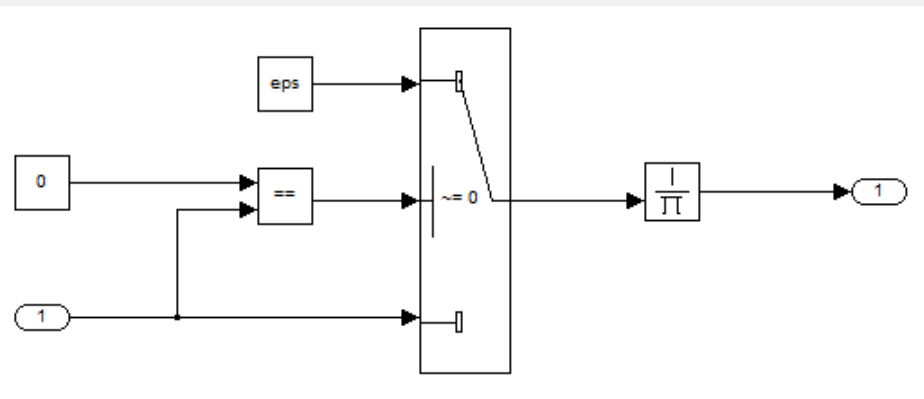
hisl_0001: Usage of Abs block

ID: Title	hisl_0001: Usage of Abs block	
Description	To support robustness of generated code, when using the Abs block,	
	A	Avoid Boolean and unsigned integer data types as inputs to the Abs block.
	B	In the Abs block parameter dialog box, select Saturate on integer overflow .
Notes	<p>The Abs block does not support Boolean data types. Specifying an unsigned input data type, might optimize the Abs block out of the generated code, resulting in a block you cannot trace to the generated code.</p> <p>For signed data types, Simulink does not represent the absolute value of the most negative value. When you select Saturate on integer overflow, the absolute value of the data type saturates to the most positive representable value. When you clear Saturate on integer overflow, absolute value calculations in the simulation and generated code might not be consistent or expected.</p>	
Rationale	A	Support generation of traceable code.
	B	Achieve consistent and expected behavior of model simulation and generated code.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for DO-178B > “Check usage of Math Operations blocks” • By Task > Modeling Standards for IEC-61508 > “Check usage of Math Operations blocks” • By Task > Modeling Standards for ISO-26262 > “Check usage of Math Operations blocks” 	

ID: Title	hisl_0001: Usage of Abs block
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.4 (3) 'Defensive programming' • IEC 61508-3, Table A.3 (2) 'Strongly typed programming language' • IEC 61508-3, Table B.8 (3) 'Control Flow Analysis' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' • ISO/DIS 26262-6, Table 7 (f) 'Control flow analysis' • DO-178B, Section 6.4.4.3c 'Structural Coverage Analysis Resolution (Dead Code)' • MISRA-C:2004, Rule 14.1 • MISRA-C:2004, Rule 21.1
Last Changed	R2011a
Examples	<div data-bbox="397 829 1184 1027"> </div> <p data-bbox="397 1050 581 1079">Recommended</p> <div data-bbox="397 1107 1184 1315"> </div> <p data-bbox="397 1340 638 1369">Not Recommended</p>

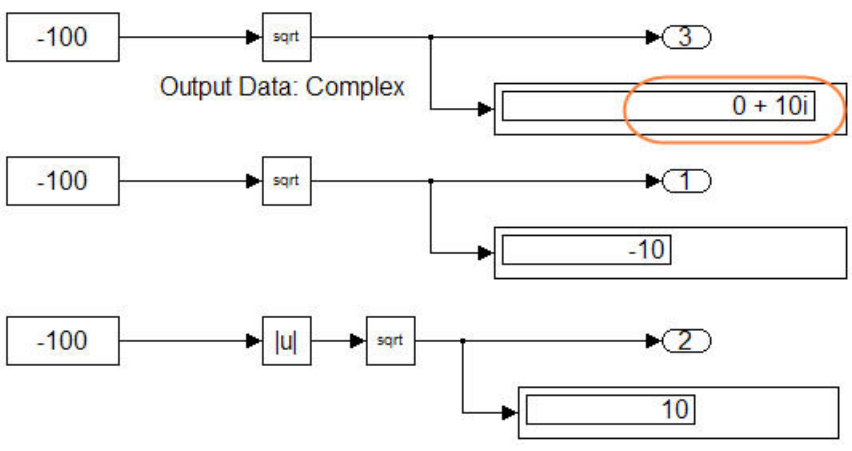
hisl_0002: Usage of Math Function blocks (remainder and reciprocal)

ID: Title	hisl_0002: Usage of Math Function blocks (remainder and reciprocal)	
Description	To support robustness of generated code, when using the Math Function block with remainder-after-division (rem) or array-reciprocal (reciprocal) functions,	
	A	Protect the input of the reciprocal function from going to zero.
	B	Protect the second input of the rem function from going to zero.
Note	You might get a divide-by-zero operation, resulting in an infinite (Inf) output value. To avoid overflows, protect the corresponding input from going to zero.	
Rationale	A, B	Protect against overflows.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check usage of Math blocks”	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262-6, Table 1(b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1(d) 'Use of defensive implementation techniques' • DO-178B, Section 6.4.2.2 'Robustness Test Cases' • DO-178B, Section 6.4.3 'Requirements-Based Testing Methods' • MISRA-C:2004, Rule 21.1 	

ID: Title	hisl_0002: Usage of Math Function blocks (remainder and reciprocal)
Last Changed	R2011a
Examples	<p>In the following example, when the input signal oscillates around zero, the output exhibits a large change in value. You need further protection against the large change in value.</p>  <p>The diagram illustrates a Simulink block diagram. It starts with a constant block labeled '0'. This signal goes to an equality comparison block '=='. The other input to the '==' block is the signal from a block labeled '1'. The output of the '==' block goes to a conditional switch block. The switch block has two inputs: the top input is the signal from the '0' block, and the bottom input is the signal from the '1' block. The switch block is controlled by two inputs: 'eps' and a signal from the '1' block. The switch block's output goes to a reciprocal block labeled $\frac{1}{\Pi}$. The final output of the reciprocal block is a constant block labeled '1'.</p>

hisl_0003: Usage of Square Root blocks

ID: Title	hisl_0003: Usage of Square Root blocks	
Description	To support robustness of generated code, when using the square root block do one of the following:	
	A	Account for complex numbers as the output.
	B	Account for negative values as the block output.
	C	Protect the input from going negative.
Notes	For negative input, the square root function takes the absolute value of the input and performs the square root operation. The square root function sets the sign of the output to negative, which might lead to undesirable results in the generated code.	
Rationale	A, B, C	Avoid undesirable results in generated code.
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262-6, Table 1(b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1(d) 'Use of defensive implementation techniques' • DO-178B, Section 6.4.2.2a 'Robustness Test Cases' 	

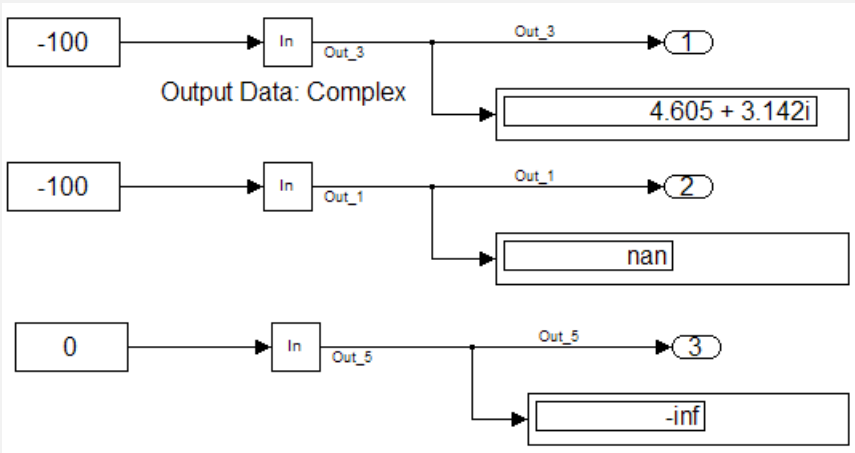
ID: Title	hisl_0003: Usage of Square Root blocks
Last Changed	R2011a
Examples	 <p>The diagram illustrates three different ways to use a square root block in a Simulink model:</p> <ul style="list-style-type: none">Top Example: A constant block with the value -100 is connected to a 'sqrt' block. The block is configured with 'Output Data: Complex'. It has two outputs: a scalar output of 3 and a complex output of 0 + 10i (circled in red).Middle Example: A constant block with the value -100 is connected to a 'sqrt' block. It has two outputs: a scalar output of 1 and a real output of -10.Bottom Example: A constant block with the value -100 is connected to an absolute value block (' u '), which is then connected to a 'sqrt' block. It has two outputs: a scalar output of 2 and a real output of 10.

hisl_0004: Usage of Math Function blocks (natural logarithm and base 10 logarithm)

ID: Title	hisl_0004: Usage of Math Function blocks (natural logarithm and base 10 logarithm)	
Description	To support robustness of generated code, when using the Math Function block with natural logarithm (log) or base 10 logarithm (log10) function parameters,	
	A	Protect the input from going negative.
	B	Protect the input from equaling zero.
	C	Account for complex numbers as the output value.
Notes	If you set the output data type to complex, the natural logarithm and base 10 logarithm functions output complex values for negative input values. If you set the output data type to real, the functions output NAN for negative numbers, and minus infinity (-inf) for zero values.	
Rationale	A, B, C	Support generation of robust code.
Model Advisor Checks	By Task > Modeling Standards for DO-178B Checks > “Check usage of Math blocks”	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262-6, Table 1(b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1(d) 'Use of defensive implementation techniques' • DO-178B, Section 6.4.2.2a 'Robustness Test Cases' • DO-178B, Sections 6.3.1g and 6.3.2g 'Algorithms are accurate' 	
Last Changed	R2011a	

ID: Title hisl_0004: Usage of Math Function blocks (natural logarithm and base 10 logarithm)

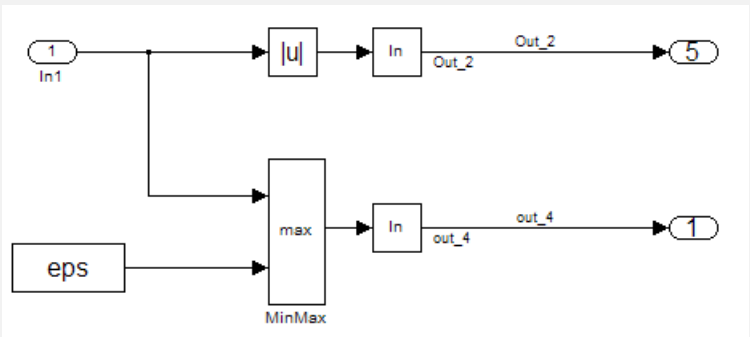
Examples

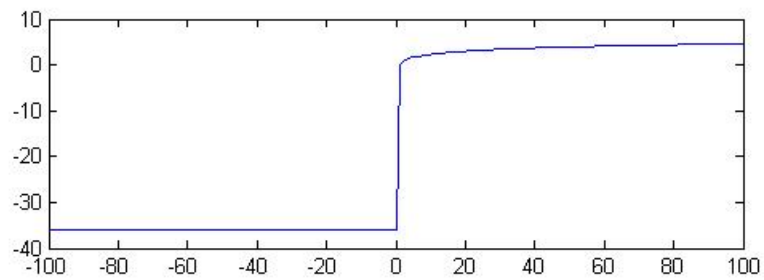
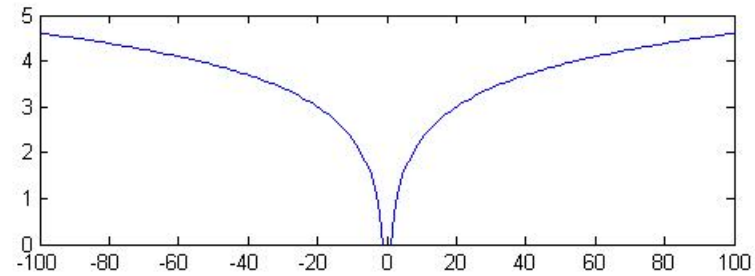


You can protect against:

- Negative numbers using an Abs block.
- Zero values using a combination of the MinMax block and a Constant block, with **Constant value** set to eps (epsilon).

The following example displays the resulting output for input values ranging from -100 to 100.



ID: Title**hisl_0004: Usage of Math Function blocks (natural logarithm and base 10 logarithm)**

hisl_0005: Usage of Product blocks

ID: Title	hisl_0005: Usage of Product blocks	
Description	To support robustness of generated code, when using the Product block with divisor inputs,	
	A	In <code>Element-wise(.*)</code> mode, protect divisor inputs from going to zero.
	B	In <code>Matrix(*)</code> mode, protect divisor inputs from becoming singular input matrices.
	C	Set the model configuration parameter Diagnostics > Data Validity > Signals > Division by singular matrix to error.
Notes	<p>When using Product blocks for element-wise divisions, you might get a divide by zero, resulting in a NaN output. To avoid overflows, protect divisor inputs from going to zero.</p> <p>When using Product blocks to compute the inverse of a matrix, or a matrix division, you might get a divide by a singular matrix. This division results in a NaN output. To avoid overflows, protect divisor inputs from becoming singular input matrices.</p> <p>During simulation, while the software inverts one of the input values of a Product block that is in matrix multiplication mode, the Division by singular matrix diagnostic can detect a singular matrix.</p>	
Rationale	A, B, C	Protect against overflows.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for signal data”	

ID: Title	hisl_0005: Usage of Product blocks
References	<ul style="list-style-type: none">• IEC 61508-3, Table A.3 (3) 'Language subset'• IEC 61508-3, Table A.4 (3) 'Defensive programming'• ISO/DIS 26262-6, Table 1(b) 'Use of language subsets'• ISO/DIS 26262-6, Table 1(d) 'Use of defensive implementation techniques'• DO-178B, Section 6.4.2.2 'Robustness Test Cases'• DO-178B, Section 6.4.3 'Requirements-Based Testing Methods'• MISRA-C:2004, Rule 21.1
Last Changed	R2011a

Ports & Subsystems

In this section...
“hisl_0006: Usage of While Iterator blocks” on page 2-15
“hisl_0007: Usage of While Iterator subsystems” on page 2-17
“hisl_0008: Usage of For Iterator Blocks” on page 2-20
“hisl_0009: Usage of For Iterator Subsystem blocks” on page 2-22
“hisl_0010: Usage of If blocks and If Action Subsystem blocks” on page 2-23
“hisl_0011: Usage of Switch Case blocks and Action Subsystem blocks” on page 2-25
“hisl_0012: Usage of conditionally executed subsystems” on page 2-27

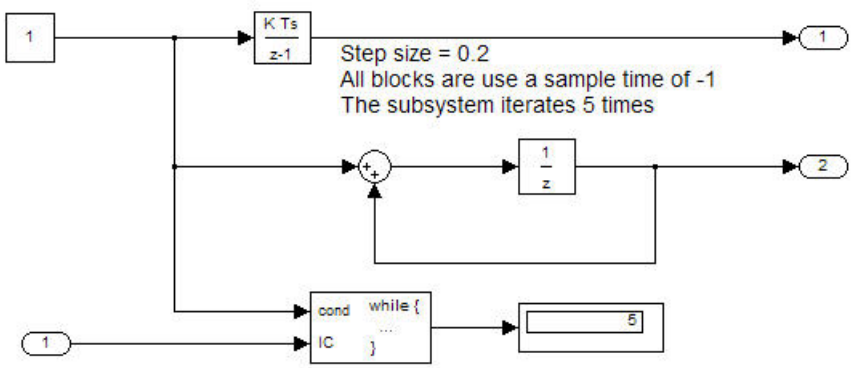
hisl_0006: Usage of While Iterator blocks

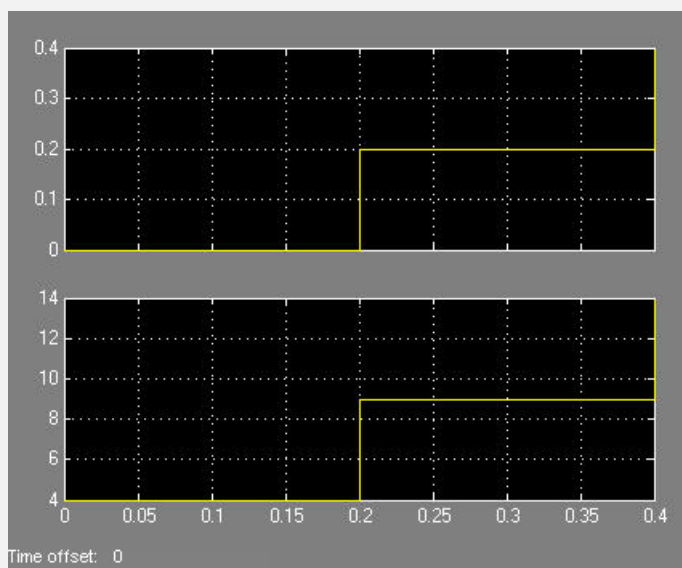
ID: Title	hisl_0006: Usage of While Iterator blocks	
Description	To support bounded iterative behavior in the generated code when using the While Iterator block, in the While Iterator block parameters dialog box:	
	A	Set Maximum number of iterations to a positive integer value; do not set value to -1 for unlimited.
	B	Consider selecting Show iteration number port to observe the iteration value during simulation.
Note	<p>When you use While Iterator subsystems, set the maximum number of iterations. If you use an unlimited number of iterations, the generated code might include infinite loops, which lead to execution-time overruns.</p> <p>To observe the iteration value during simulation and determine whether the loop reaches the maximum number of iterations, select the While Iterator block parameter Show iteration number port. If the loop reaches the maximum number of iterations, verify the output values of the While Iterator block.</p>	
Rationale	A, B	Support bounded iterative in the generated code.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for IEC 61508 > “Check usage of Ports and Subsystems blocks” • By Task > Modeling Standards for ISO 26262 > “Check usage of Ports and Subsystems blocks” • By Task > Modeling Standards for DO-178B > “Check usage of Ports and Subsystems blocks” 	

ID: Title	hisl_0006: Usage of While Iterator blocks
References	<ul style="list-style-type: none">• IEC 61508-3, Table A.3 (3) 'Language subset'• IEC 61508-3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets'• ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' • DO-178B, Section 6.3.1e 'Review and Analyses of the High-Level Requirements: Conformance to standards'• DO-178B, Section 6.3.2e 'Review and Analyses of the Low-Level Requirements: Conformance to standards' • MISRA-C:2004, Rule 21.1
Last Changed	R2011a

hisl_0007: Usage of While Iterator subsystems

ID: Title	hisl_0007: Usage of While Iterator subsystems	
Description	To support unambiguous behavior, when using While Iterator subsystems,	
	A	Specify inherited (-1) or constant (inf) sample times for the blocks within the subsystems.
	B	Avoid using sample time-dependent blocks, such as integrators, filters, and transfer functions, within the subsystems.
Rationale	A, B	Avoid ambiguous behavior from the subsystem.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for IEC 61508 > “Check usage of Ports and Subsystems blocks” • By Task > Modeling Standards for ISO 26262 > “Check usage of Ports and Subsystems blocks” • By Task > Modeling Standards for DO-178B > “Check usage of Ports and Subsystems blocks” 	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' • DO-178B, Section 6.3.1e 'Review and Analyses of the High-Level Requirements: Conformance to standards' • DO-178B, Section 6.3.2e 'Review and Analyses of the Low-Level Requirements: Conformance to standards' • MISRA-C:2004, Rule 21.1 	

ID: Title	hisl_0007: Usage of While Iterator subsystems
Last Changed	R2011a
Examples	<p>For iterative subsystems, the value <code>delta T</code> is nonzero for the first iteration only. For subsequent iterations, the value is zero.</p> <p>In the following example, in the output of the Sum block calculation that uses the unit delay, the Sum block calculation does not require <code>delta T</code>. The output of the Discrete-Time Integrator block displays the result of having a zero <code>delta T</code> value.</p>  <p>The diagram illustrates a Simulink subsystem enclosed in a 'while' loop. The loop is controlled by a 'cond' block with an initial condition (IC) of 1. The loop body contains the following blocks and connections:</p> <ul style="list-style-type: none"> An input signal '1' splits into three paths: <ul style="list-style-type: none"> Top path: A block labeled $\frac{K T_s}{z-1}$ with an output scope labeled '1'. Middle path: A summing junction (+) that receives the signal from the top path and the output of the discrete-time integrator block. Bottom path: A 'cond while' block with an IC of 1, which outputs the value 5 to a scope. The output of the summing junction goes to a discrete-time integrator block labeled $\frac{1}{z}$. The output of the integrator block is fed back to the summing junction and also goes to an output scope labeled '2'. <p>Text annotations in the diagram state: "Step size = 0.2", "All blocks are use a sample time of -1", and "The subsystem iterates 5 times".</p>

ID: Title**hisl_0007: Usage of While Iterator subsystems**

hisl_0008: Usage of For Iterator Blocks

ID: Title	hisl_0008: Usage of For Iterator blocks	
Description	To support bounded iterative behavior in the generated code when using the For Iterator block, do one of the following:	
	A	In the For Iterator block parameters dialog box, set Iteration limit source to internal .
	B	If Iteration limit source must be external , use a block that has a constant value, such as a Width, Probe, or Constant.
	C	In the For Iterator block parameters dialog box, clear Set next i (iteration variable) externally .
	D	In the For Iterator block parameters dialog box, consider selecting Show iteration variable to observe the iteration value during simulation.
Notes	When you use the For Iterator block, feed the loop control variable with fixed (nonvariable) values to get a predictable number of loop iterations. Otherwise, a loop can result in unpredictable execution times and, in the case of external iteration variables, infinite loops that can lead to execution-time overruns.	
Rationale	A, B, C, D	Support bounded iterative behavior in generated code.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for IEC 61508 > “Check usage of Ports and Subsystems blocks” • By Task > Modeling Standards for ISO 26262 > “Check usage of Ports and Subsystems blocks” • By Task > Modeling Standards for DO-178B > “Check usage of Ports and Subsystems blocks” 	

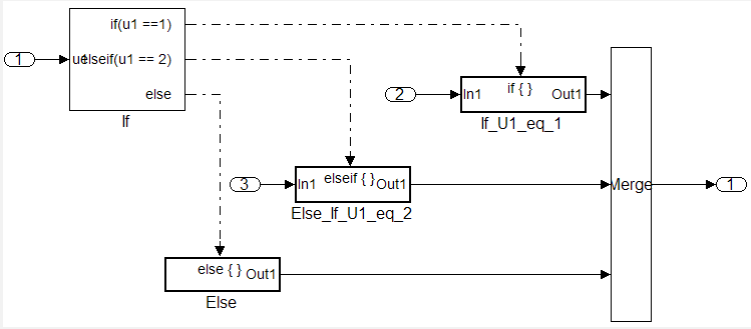
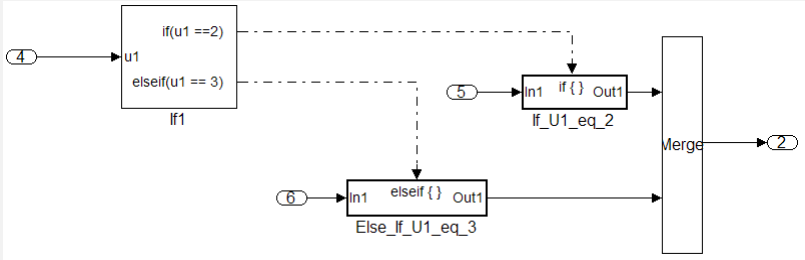
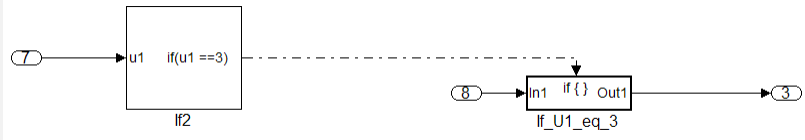
ID: Title	hisl_0008: Usage of For Iterator blocks
References	<ul style="list-style-type: none">• IEC 61508-3, Table A.3 (3) 'Language subset'• IEC 61508-3, Table A.4 (3) 'Defensive programming'• ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets'• ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques'• DO-178B, Section 6.3.1e 'Review and Analyses of the High-Level Requirements: Conformance to standards'• DO-178B, Section 6.3.2e 'Review and Analyses of the Low-Level Requirements: Conformance to standards'• MISRA-C:2004, Rule 13.6
Last Changed	R2011a

hisl_0009: Usage of For Iterator Subsystem blocks

ID: Title	hisl_0009: Usage of For Iterator Subsystem blocks	
Description	To support unambiguous behavior, when using the For Iterator Subsystem block,	
	A	Specify inherited (-1) or constant (inf) sample times for blocks within the subsystem.
	B	Avoid using sample time-dependent blocks, such as integrators, filters, and transfer functions, within the subsystem.
Rationale	A, B	Avoid ambiguous behavior from the subsystem.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for IEC 61508 > “Check usage of Ports and Subsystems blocks” • By Task > Modeling Standards for ISO 26262 > “Check usage of Ports and Subsystems blocks” • By Task > Modeling Standards for DO-178B > “Check usage of Ports and Subsystems blocks” 	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset'; IEC 61508-3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' • DO-178B, Section 6.4.2.2d 'Robustness Test Cases: (For Loops)' • MISRA-C:2004, Rule 13.6 	
Last Changed	R2011a	
Examples	See “hisl_0007: Usage of While Iterator subsystems” on page 2-17.	

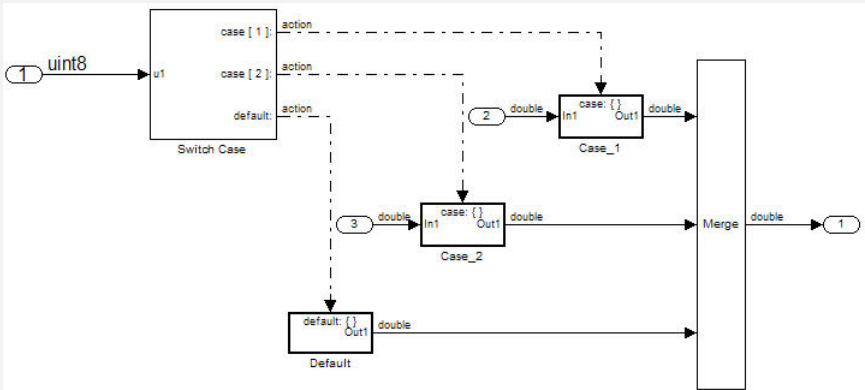
hisl_0010: Usage of If blocks and If Action Subsystem blocks

ID: Title	hisl_0010: Usage of If blocks and If Action Subsystem blocks	
Description	To support verifiable generated code, when using the If block with nonempty <code>Elseif</code> expressions,	
	A	In the block parameter dialog box, select Show else condition .
	B	Connect the outports of the If block to If Action Subsystem blocks.
Prerequisites	“hisl_0016: Usage of blocks that compute relational operators” on page 2-39	
Notes	The combination of If and If Action Subsystem blocks enable conditional execution based on input conditions. When there is only an <code>if</code> branch, you do not need to include an <code>else</code> branch.	
Rationale	A, B	Support generation of verifiable code.
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262–6, Table 1(b) 'Use of language subsets' • ISO/DIS 26262–6, Table 1(d) 'Use of defensive implementation techniques' • MISRA-C:2004, Rule 14.10 	
See Also	na_0012: Use of Switch vs. If-Then-Else Action Subsystem in the Simulink documentation	
Last Changed	R2011a	

ID: Title	hisl_0010: Usage of If blocks and If Action Subsystem blocks
Examples	 <p>Recommended: Elseif with Else</p>
	 <p>Not Recommended: No Else Path</p>
	 <p>Recommended: Only an If, no Else required</p>

hisl_0011: Usage of Switch Case blocks and Action Subsystem blocks

ID: Title	hisl_0011: Usage of Switch Case blocks and Action Subsystem blocks	
Description	To support verifiable generated code, when using the Switch Case block:	
	A	In the Switch Case block parameter dialog box, select Show default case .
	B	Connect the outputs of the Switch Case block to an If Action Subsystem block.
	C	Use an integer data type for the inputs to Switch Case blocks.
Prerequisites	“hisl_0016: Usage of blocks that compute relational operators” on page 2-39	
Notes	The combination of Switch Case and If Action Subsystem blocks enable conditional execution based on input conditions. Provide a default path of execution in the form of a “Default” block.	
Rationale	A, B, C	Support generation of verifiable code.
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262–6, Table 1(b) 'Use of language subsets' • ISO/DIS 26262–6, Table 1(d) 'Use of defensive implementation techniques' • MISRA-C:2004, Rule 14.10 	
See Also	db_0115: Simulink patterns for case constructs in the Simulink documentation.	

<p>ID: Title</p>	<p>hisl_0011: Usage of Switch Case blocks and Action Subsystem blocks</p>
<p>Last Changed</p>	<p>R2011a</p>
<p>Examples</p>	<p>The following graphic displays an example of providing a default path of execution using a “Default” block.</p>  <p>The diagram illustrates a signal flow starting with a 'uint8' input (labeled 1) entering a 'Switch Case' block. This block has three outputs: 'case [1]', 'case [2]', and 'default:'. Each output is connected to an 'action' block. The outputs of these 'action' blocks are connected to three parallel paths: 'Case_1', 'Case_2', and 'Default'. Each path consists of an 'action' block, an 'in1'/'Out1' block, and a 'double' output. The outputs of these three paths are combined in a 'Merge' block, which then outputs a 'double' signal (labeled 1).</p>

hisl_0012: Usage of conditionally executed subsystems

ID: Title	hisl_0012: Usage of conditionally executed subsystems	
Description	To support unambiguous behavior, when using conditionally executed subsystems:	
	A	Specify inherited (-1) sample times for all blocks in the subsystem, except Constant. Constant blocks can use infinite (inf) sample time.
	B	If the subsystem is called asynchronously, avoid using sample time-dependent blocks, such as integrators, filters, and transfer functions, within the subsystem.
Notes	<p>Conditionally executed subsystems include:</p> <ul style="list-style-type: none"> • If Action • Switch Case Action • Function-Call • Triggered • Enabled <p>Sample time-dependent blocks include:</p> <ul style="list-style-type: none"> • Discrete State-Space • Discrete-Time Integrator • Discrete FIR Filter • Discrete Filter • Discrete Transfer Fcn • Discrete Zero-Pole • Transfer Fcn First Order • Transfer Fcn Real Zero • Transfer Fcn Lead or Lag 	
Rationale	A, B	Support unambiguous behavior.

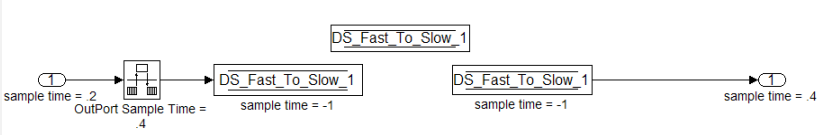
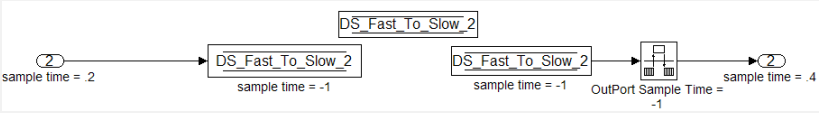
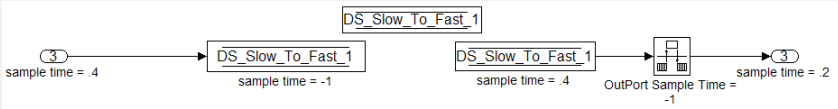
ID: Title	hisl_0012: Usage of conditionally executed subsystems
References	<ul style="list-style-type: none">• IEC 61508-3, Table A.3 (3) 'Language subset'• IEC 61508-3, Table A.4 (3) 'Defensive programming'• ISO/DIS 26262-6, Table 1(b) 'Use of language subsets'• ISO/DIS 26262-6, Table 1(d) 'Use of defensive implementation techniques'
Last Changed	R2011b
Examples	When using discrete blocks, the behavior depends on the operation across multiple contiguous time steps. When the blocks are called intermittently, the results may not conform to your expectations.

Signal Routing

In this section...
“hisl_0013: Usage of data store blocks” on page 2-30
“hisl_0015: Usage of Merge blocks” on page 2-33
“hisl_0021: Consistent vector indexing method” on page 2-35
“hisl_0022: Data type selection for index signals” on page 2-36
“hisl_0023: Verification of model and subsystem variants” on page 2-37

hisl_0013: Usage of data store blocks

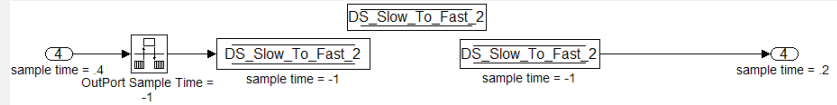
ID: Title	hisl_0013: Usage of data store blocks	
Description	To support deterministic behavior across different sample times or models when using data store blocks, including Data Store Memory, Data Store Read, and Data Store Write:	
	A	In the Configuration Parameters dialog box, on the Diagnostics > Data Validity pane, under Data Store Memory Block , set the following parameters to error: <ul style="list-style-type: none"> • Detect read before write • Detect write after read • Detect write after write • Multitask data store • Duplicate data store names
	B	Avoid data store reads and writes that occur across model and atomic subsystem boundaries.
	C	Avoid using data stores to write and read data at different rates, because different rates can result in inconsistent exchanges of data. To provide deterministic data coupling in multirate systems, use Rate Transition blocks before Data Store Write blocks, or after Data Store Read blocks.
Notes	The sorting algorithm in Simulink does not take into account data coupling between models and atomic subsystems. Using data store memory blocks can have significant impact on your software verification effort. Models and subsystems that use only inports and outports to pass data provide a directly traceable interface, simplifying the verification process.	
Rationale	A, B, C	Support consistent data values across different sample times or models.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for data store memory”	

ID: Title	hisl_0013: Usage of data store blocks
References	<ul style="list-style-type: none"> IEC 61508-3, Table A.3 (3) 'Language subset' IEC 61508-3, Table A.4 (3) 'Defensive programming' ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' DO-178B, Section 6.3.3b 'Review and Analyses of the Software Architecture: Consistency'
Last Changed	R2011a
Examples	<p>The following examples use Rate Transition blocks to provide deterministic data coupling in multirate systems</p> <ul style="list-style-type: none"> For fast-to-slow transitions: <p>Set the rate of the slow sample time on either the Rate Transition block or the Data Store Write block.</p>  <p>Do not place the Rate Transition block after the Data Store Read block.</p>  For slow-to-fast transitions: <p>If the Rate Transition block is after the Data Store Read block, specify the slow rate on the Data Store Read block.</p> 

ID: Title

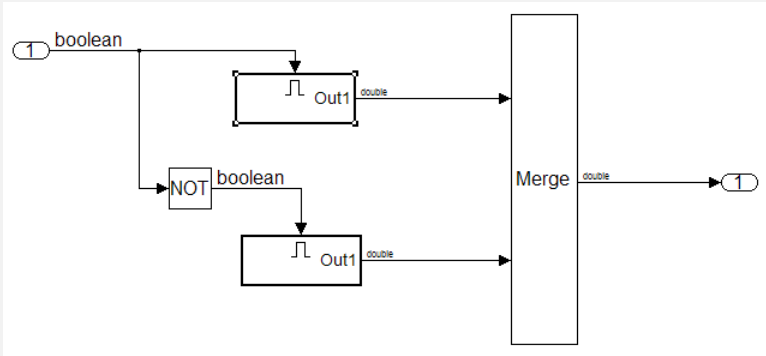
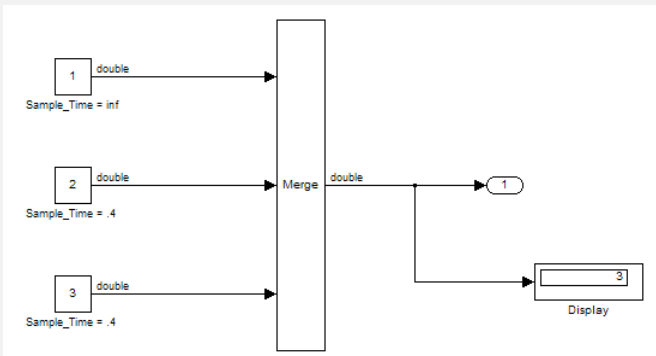
hisl_0013: Usage of data store blocks

If the Rate Transition block is before the Data Store Write block, use the inherited sample time for the blocks.



hisl_0015: Usage of Merge blocks

ID: Title	hisl_0015: Usage of Merge blocks	
Description	To support unambiguous behavior from Merge blocks,	
	A	Use Merge blocks only with conditionally executed subsystems.
	B	Specify execution of the conditionally executed subsystems such that only one subsystem executes during a time step.
	C	Clear the Merge block parameter Allow unequal port widths .
Notes	<p>Simulink combines the inputs of the Merge block into a single output. The output value at any time is equal to the most recently computed output of the blocks that drive the Merge block. Therefore, the Merge block output is dependent upon the execution order of the input computations.</p> <p>To provide predictable behavior of the Merge block output, you must have mutual exclusion between the conditionally executed subsystems feeding a Merge block. If the inputs are not mutually exclusive, Simulink uses the last input port.</p>	
Rationale	A, B, C	Avoid unambiguous behavior.
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262-6, Table 1(b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1(d) 'Use of defensive implementation techniques' • DO-178B, Section 6.3.3b 'Reviews and Analyses of the Software Architecture: Consistency' 	
Last Changed	R2011a	

ID: Title	hisl_0015: Usage of Merge blocks
Examples	 <p>Recommended</p>
	 <p>Not Recommended</p>

hisl_0021: Consistent vector indexing method

ID: Title	hisl_0021: Consistent vector indexing method			
Description	Within a model, use: <table border="1" data-bbox="390 413 1335 713"> <tr> <td data-bbox="390 413 457 713">A</td> <td data-bbox="457 413 1335 713"> A consistent vector indexing method for allblocks. Blocks for which you should set the indexing method include: <ul style="list-style-type: none"> • Index Vector • Multiport Switch • Assignment • Selector • For Iterator </td> </tr> </table>		A	A consistent vector indexing method for allblocks. Blocks for which you should set the indexing method include: <ul style="list-style-type: none"> • Index Vector • Multiport Switch • Assignment • Selector • For Iterator
A	A consistent vector indexing method for allblocks. Blocks for which you should set the indexing method include: <ul style="list-style-type: none"> • Index Vector • Multiport Switch • Assignment • Selector • For Iterator 			
Rationale	A	Reduce the risk of introducing errors due to inconsistent indexing.		
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for IEC 61508 > “Check for inconsistent vector indexing methods” • By Task > Modeling Standards for ISO 26262 > “Check for inconsistent vector indexing methods” • By Task > Modeling Standards for DO-178B > “Check for inconsistent vector indexing methods” 			
References	<ul style="list-style-type: none"> • DO-178B, Section 6.3.2b 'Accuracy and Consistency of Low-Level Requirements' • IEC 61508–3, Table A.3 (3) 'Language subset' IEC 61508–3, Table A.4 (5) 'Design and coding standards' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' ISO/DIS 26262-6, Table 1 (f) 'Use of unambiguous graphical representation' 			
See Also	“cgsl_0101: Zero-based indexing”			
Last Changed	R2012a			

hisl_0022: Data type selection for index signals

ID: Title	hisl_0022: Data type selection for index signals	
Description	For index signals, use:	
	A	An integer or enumerated data type
	B	A data type that covers the range of indexed values.
Rationale	Blocks that use a signal index include: <ul style="list-style-type: none"> • Assignment • Direct Lookup Table (n-D) • Index Vector • Interpolation Using Prelookup • MATLAB Function • Multiport Switch • n-D Lookup Table (internal type index selection) • Selector • Stateflow Chart 	
	A	Prevent unexpected results that can occur with rounding operations for floating-point data types.
	B	Enable access to data in a vector.
References	<ul style="list-style-type: none"> • IEC 61508–3, Table A.3 (2) 'Strongly typed programming language' • IEC 61508–3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1 (c) 'Enforcement of strong typing' • ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' • DO-178B, Section 6.3.4f 'Accuracy and Consistency of Source Code' 	
Last Changed	R2011b	

hisl_0023: Verification of model and subsystem variants

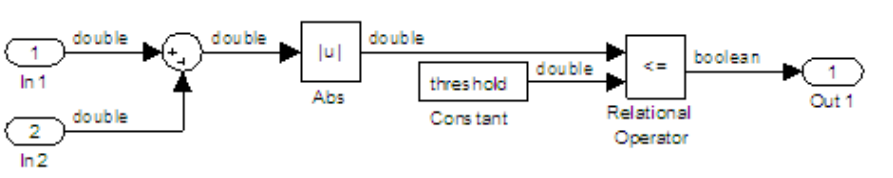
ID: Title	hisl_0023: Verification of model and subsystem variants	
Description	When verifying that a model is consistent with generated code, do one of the following:	
	A	In the Configuration Parameters dialog box, on the Code Generation > Interface pane, disable variants in generated code by setting Generate preprocessor conditionals to <code>Disable all</code> .
	B	Verify all combinations of model variants that might be active in the generated code.
Rationale	A	Simplify consistency testing between the model and generated code by restricting the code base to a single variant.
	B	Make sure that consistency testing between the model and generated code is complete for all variants.
References	<ul style="list-style-type: none"> • DO-178B, Section 6.4.4.2 'Structural Coverage Analysis and Section' DO-178B, Section 6.4.4.3 'Structural Coverage Analysis Resolution' • IEC 61508–3, Table A.4 (7) 'Use of trusted / verified software modules and components' 	
Last Changed	R2010b	

Logic and Bit Operations

In this section...
“hisl_0016: Usage of blocks that compute relational operators” on page 2-39
“hisl_0017: Usage of blocks that compute relational operators (2)” on page 2-41
“hisl_0018: Usage of Logical Operator block” on page 2-42
“hisl_0019: Usage of Bitwise Operator block” on page 2-43

hisl_0016: Usage of blocks that compute relational operators

ID: Title	hisl_0016: Usage of blocks that compute relational operators	
Description	To support the robustness of the operations, when using blocks that compute relational operators, including Relational Operator, Compare To Constant, Compare to Zero, and Detect Change	
	A	Avoid comparisons using the == or ~= operator on floating-point data types.
Notes	<p>Due to floating-point precision issues, do not test floating-point expressions for equality (==) or inequality (≠).</p> <p>When the model contains a block computing a relational operator with the == or ~= operators, the inputs to the block must not be single, double, or any custom storage class that is a floating-point type. Change the data type of the input signals, or rework the model to eliminate using the == or ~= operators within blocks that compute relational operators.</p>	
Rationale	A	Improve model robustness.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for IEC 61508 > “Check usage of Logic and Bit Operations blocks” • By Task > Modeling Standards for ISO 26262 > “Check usage of Logic and Bit Operations blocks” • By Task > Modeling Standards for DO-178B > “Check usage of Logic and Bit Operations blocks” 	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.4 (3) 'Defensive programming' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' • DO-178B, Section 6.3.1g 'Algorithms are accurate' • DO-178B, Section 6.3.2g 'Algorithms are accurate' • MISRA-C:2004, Rule 13.3 	
See Also	“hisl_0017: Usage of blocks that compute relational operators (2)” on page 2-41	

ID: Title	hisl_0016: Usage of blocks that compute relational operators
Last Changed	R2011a
Examples	<p>Positive Pattern: To test whether two floating-point variables or expressions are equal, compare the difference of the two variables against a threshold that takes into account the floating-point relative accuracy (eps) and the magnitude of the numbers.</p> <p>The following pattern shows how to test two double-precision input signals, In1 and In2, for equality.</p> 

hisl_0017: Usage of blocks that compute relational operators (2)

ID: Title	hisl_0017: Usage of blocks that compute relational operators (2)	
Description	To support unambiguous behavior in the generated code, when using blocks that compute relational operators, including Relational Operator, Compare To Constant, Compare to Zero, and Detect Change	
	A	Set the block Output data type parameter to Boolean.
Rationale	A	Support generation of code that produces unambiguous behavior.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for IEC 61508 > “Check usage of Logic and Bit Operations blocks” • By Task > Modeling Standards for ISO 26262 > “Check usage of Logic and Bit Operations blocks” • By Task > Modeling Standards for DO-178B > “Check usage of Logic and Bit Operations blocks” 	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset'; IEC 61508-3, Table A.3 (2) 'Strongly typed programming language' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' ISO/DIS 26262-6, Table 1 (c) 'Enforcement of strong typing' • DO-178B, Section 6.3.1g 'Algorithms are accurate' DO-178B, Section 6.3.2g 'Algorithms are accurate' • MISRA-C:2004, Rule 12.6 	
See Also	“hisl_0016: Usage of blocks that compute relational operators” on page 2-39	
Last Changed	R2011a	

hisl_0018: Usage of Logical Operator block

ID: Title	hisl_0018: Usage of Logical Operator block	
Description	To support unambiguous behavior of generated code, when using the Logical Operator block,	
	A	Set the Output data type block parameter to Boolean.
Prerequisites	“hisl_0045: Configuration Parameters > Optimization > Implement logic signals as Boolean data (vs. double)” on page 3-25	
Rationale	A	Avoid ambiguous behavior of generated code.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for IEC 61508 > “Check usage of Logic and Bit Operations blocks” • By Task > Modeling Standards for ISO 26262 > “Check usage of Logic and Bit Operations blocks” • By Task > Modeling Standards for DO-178B > “Check usage of Logic and Bit Operations blocks” • By Task > Modeling Standards for DO-178B > “Check safety-related optimization settings” 	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.3 (2) 'Strongly typed programming language' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1 (c) 'Enforcement of strong typing' • DO-178B, Section 6.3.1g 'Algorithms are accurate' • DO-178B, Section 6.3.2g 'Algorithms are accurate' • MISRA-C:2004, Rule 12.6 	
Last Changed	R2011a	

hisl_0019: Usage of Bitwise Operator block

ID: Title	hisl_0019: Usage of Bitwise Operator block	
Description	To support unambiguous behavior, when using the Bitwise Operator block,	
	A	Avoid signed integer data types as input to the block.
	B	Choose an output data type that represents zero exactly.
Notes	Bitwise operations on signed integers are not meaningful. If a shift operation moves a signed bit into a numeric bit, or a numeric bit into a signed bit, unpredictable and unwanted behavior can result.	
Rationale	A, B	Support unambiguous behavior of generated code.
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.3 (2) 'Strongly typed programming language' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1 (c) 'Enforcement of strong typing' • ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' • MISRA-C:2004, Rule 12.7 	
See Also	"hisf_0003: Usage of bitwise operations" on page 4-11 in the Simulink documentation	
Last Changed	R2011a	

Configuration Parameter Considerations

- “Solver” on page 3-2
- “Diagnostics” on page 3-7
- “Optimizations” on page 3-24

Solver

In this section...
“hisl_0040: Configuration Parameters > Solver > Simulation time” on page 3-3
“hisl_0041: Configuration Parameters > Solver > Solver options” on page 3-4
“hisl_0042: Configuration Parameters > Solver > Tasking and sample time options” on page 3-5

hisl_0040: Configuration Parameters > Solver > Simulation time

ID: Title	hisl_0040: Configuration Parameters > Solver > Simulation time	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Solver pane, set parameters for simulation time as follows:	
	A	Start time to 0.0.
	B	Stop time to a positive value that is less than the value of Application lifespan (days) .
Note	<p>Simulink allows nonzero start times for simulation. However, production code generation requires a zero start time.</p> <p>By default, Application lifespan (days) is <code>inf</code>. If you do not change this setting, any positive value for Stop time is valid.</p> <p>You specify Stop time in seconds and Application lifespan (days) is in days.</p>	
Rationale	A	Generate code that is valid for production code generation.
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' 	
See Also	<ul style="list-style-type: none"> • “hisl_0048: Configuration Parameters > Optimization > Application lifespan (days)” on page 3-28 • Solver Pane section of the Simulink documentation 	
Last Changed	R2011a	

hisl_0041: Configuration Parameters > Solver > Solver options

ID: Title	hisl_0041: Configuration Parameters > Solver > Solver options	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Solver pane, set parameters for solvers as follows:	
	A	Type to Fixed-step.
	B	Solver to discrete (no continuous states).
Note	Generating code for production requires a fixed-step, discrete solver.	
Rationale	A, B	Generate code that is valid for production code generation.
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' 	
See Also	"Solver Pane" in the Simulink documentation	
Last Changed	R2011a	

hisl_0042: Configuration Parameters > Solver > Tasking and sample time options

ID: Title	hisl_0042: Configuration Parameters > Solver > Tasking and sample time options	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Solver pane, set parameters for tasking and sample time as follows:	
	A	<p>Periodic sample time constraint to Specified and assign values to Sample time properties.</p> <hr/> <p>Caution If you use a referenced model as a reusable function, set Periodic sample time constraint to Ensure sample time independent.</p> <hr/>
	B	Tasking mode for periodic sample times to SingleTasking or MultiTasking.
	C	Clear the parameter Automatically handle data transfers between tasks .
Notes	<p>Selecting the Automatically handle data transfers between tasks check box might result in inserting rate transition code without a corresponding model construct. This might impede establishing full traceability or showing that unintended functions are not introduced.</p> <p>You can select or clear the Higher priority value indicates higher task priority check box . Selecting this check box determines whether the priority for Sample time properties uses the lowest values as highest priority, or the highest values as highest priority.</p>	
Rationale	A, B, C	Support fully specified models and unambiguous code.
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • DO-178B, Section 6.3.4e 'Source code is traceable to low-level requirements' 	

ID: Title	hisl_0042: Configuration Parameters > Solver > Tasking and sample time options
See Also	“Solver Pane” in the Simulink documentation
Last Changed	R2011a

Diagnostics

In this section...

“hisl_0043: Configuration Parameters > Diagnostics > Solver” on page 3-8

“hisl_0044: Configuration Parameters > Diagnostics > Sample Time” on page 3-10

“hisl_0301: Configuration Parameters > Diagnostics > Compatibility” on page 3-13

“hisl_0302: Configuration Parameters > Diagnostics > Data Validity > Parameters” on page 3-14

“hisl_0303: Configuration Parameters > Diagnostics > Data Validity > Merge block” on page 3-15

“hisl_0304: Configuration Parameters > Diagnostics > Data Validity > Model Initialization” on page 3-16

“hisl_0305: Configuration Parameters > Diagnostics > Data Validity > Debugging” on page 3-17

“hisl_0306: Configuration Parameters > Diagnostics > Connectivity > Signals” on page 3-18

“hisl_0307: Configuration Parameters > Diagnostics > Connectivity > Buses” on page 3-19

“hisl_0308: Configuration Parameters > Diagnostics > Connectivity > Function calls” on page 3-20

“hisl_0309: Configuration Parameters > Diagnostics > Type Conversion” on page 3-21

“hisl_0310: Configuration Parameters > Diagnostics > Model Referencing” on page 3-22

“hisl_0311: Configuration Parameters > Diagnostics > Stateflow” on page 3-23

hisl_0043: Configuration Parameters > Diagnostics > Solver

ID: Title	hisl_0043: Configuration Parameters > Diagnostics > Solver									
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Solver section to:									
	Compile-Time	<ul style="list-style-type: none"> • Algebraic loop to error. • Minimize algebraic loop to error. • Unspecified inheritability of sample times to error. • Automatic solver parameter selection to error. • State name clash to warning. 								
	Run-Time	<ul style="list-style-type: none"> • Block priority violation to error if you are using block priorities. 								
Note	<p>Enabling diagnostics pertaining to the solver provides information to detect violations of other guidelines.</p> <table border="1" data-bbox="397 951 1313 1428"> <thead> <tr> <th data-bbox="397 951 857 1001">If Diagnostic Parameter...</th> <th data-bbox="863 951 1313 1001">Is Not Set As Indicated, Then ...</th> </tr> </thead> <tbody> <tr> <td data-bbox="397 1005 857 1142">Algebraic loop</td> <td data-bbox="863 1005 1313 1142">Automatic breakage of algebraic loops can go undetected and might result in unpredictable block order execution.</td> </tr> <tr> <td data-bbox="397 1145 857 1289">Minimize algebraic loop</td> <td data-bbox="863 1145 1313 1289">Automatic breakage of algebraic loops can go undetected and might result in unpredictable block order execution.</td> </tr> <tr> <td data-bbox="397 1293 857 1428">Block priority violation</td> <td data-bbox="863 1293 1313 1428">Block execution order can include undetected conflicts that might</td> </tr> </tbody> </table>		If Diagnostic Parameter...	Is Not Set As Indicated, Then ...	Algebraic loop	Automatic breakage of algebraic loops can go undetected and might result in unpredictable block order execution.	Minimize algebraic loop	Automatic breakage of algebraic loops can go undetected and might result in unpredictable block order execution.	Block priority violation	Block execution order can include undetected conflicts that might
If Diagnostic Parameter...	Is Not Set As Indicated, Then ...									
Algebraic loop	Automatic breakage of algebraic loops can go undetected and might result in unpredictable block order execution.									
Minimize algebraic loop	Automatic breakage of algebraic loops can go undetected and might result in unpredictable block order execution.									
Block priority violation	Block execution order can include undetected conflicts that might									

ID: Title	hisl_0043: Configuration Parameters > Diagnostics > Solver							
	<p>result in unpredictable block order execution.</p> <table border="1" data-bbox="397 388 1322 826"> <tr> <td data-bbox="397 388 857 604">Unspecified inheritability of sample times</td> <td data-bbox="862 388 1322 604">An S-function that is not explicitly set to inherit sample time can go undetected and result in unpredictable behavior.</td> </tr> <tr> <td data-bbox="397 609 857 743">Automatic solver parameter selection</td> <td data-bbox="862 609 1322 743">An automatic change to the solver, step size, or simulation stop time can go undetected and might the operation of generated code.</td> </tr> <tr> <td data-bbox="397 748 857 826">State name clash</td> <td data-bbox="862 748 1322 826">A name being used for more than one state might go undetected.</td> </tr> </table> <p>You can set the following solver diagnostic parameters to anyvalue:</p> <ul style="list-style-type: none"> Min step size violation Sample hit time adjusting Consecutive zero crossings violation Solver data inconsistency Extraneous discrete derivative signals 		Unspecified inheritability of sample times	An S-function that is not explicitly set to inherit sample time can go undetected and result in unpredictable behavior.	Automatic solver parameter selection	An automatic change to the solver, step size, or simulation stop time can go undetected and might the operation of generated code.	State name clash	A name being used for more than one state might go undetected.
Unspecified inheritability of sample times	An S-function that is not explicitly set to inherit sample time can go undetected and result in unpredictable behavior.							
Automatic solver parameter selection	An automatic change to the solver, step size, or simulation stop time can go undetected and might the operation of generated code.							
State name clash	A name being used for more than one state might go undetected.							
Rationale	A	Support generation of robust and unambiguous code.						
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for solvers”							
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • DO-178B, 6.3.3e 'Software architecture conforms to standards' 							
See Also	<ul style="list-style-type: none"> • “Diagnostics Pane: Solver” in the Simulink documentation • jc_0021: Model diagnostic settings in the Simulink documentation 							
Last Changed	R2012a							

hisl_0044: Configuration Parameters > Diagnostics > Sample Time

ID: Title	hisl_0044: Configuration Parameters > Diagnostics > Sample Time							
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Sample Time section to error:							
	Compile-Time	<ul style="list-style-type: none"> • Source block specifies -1 sample time • Discrete used as continuous • Multitask rate transition • Single task rate transition • Multitask conditionally executed subsystem • Tasks with equal priority • Enforce sample times specified by Signal Specification blocks <p>If the target system does not allow preemption between tasks that have equal priority, set Tasks with equal priority to none.</p>						
	Run-Time	Not applicable						
Note	<p>Enabling diagnostics pertaining to the solver provides information to detect violations of other guidelines.</p> <table border="1" data-bbox="397 1177 1325 1543"> <thead> <tr> <th data-bbox="397 1177 857 1225">If Diagnostic Parameter...</th> <th data-bbox="862 1177 1325 1225">Is Not Set As Indicated, Then ...</th> </tr> </thead> <tbody> <tr> <td data-bbox="397 1230 857 1399">Source block specifies -1 sample time</td> <td data-bbox="862 1230 1325 1399">Use of inherited sample times for a source block, such as Sine Wave, can go undetected and result in unpredictable execution rates for source and downstream blocks.</td> </tr> <tr> <td data-bbox="397 1404 857 1543">Discrete used as continuous</td> <td data-bbox="862 1404 1325 1543">Input signals with continuous sample times for a discrete block, such as Unit Delay, can go undetected. You cannot use signals</td> </tr> </tbody> </table>		If Diagnostic Parameter...	Is Not Set As Indicated, Then ...	Source block specifies -1 sample time	Use of inherited sample times for a source block, such as Sine Wave, can go undetected and result in unpredictable execution rates for source and downstream blocks.	Discrete used as continuous	Input signals with continuous sample times for a discrete block, such as Unit Delay, can go undetected. You cannot use signals
If Diagnostic Parameter...	Is Not Set As Indicated, Then ...							
Source block specifies -1 sample time	Use of inherited sample times for a source block, such as Sine Wave, can go undetected and result in unpredictable execution rates for source and downstream blocks.							
Discrete used as continuous	Input signals with continuous sample times for a discrete block, such as Unit Delay, can go undetected. You cannot use signals							

ID: Title		hisl_0044: Configuration Parameters > Diagnostics > Sample Time
		with continuous sample times for embedded real-time software applications
	Multitask rate transition	Invalid rate transitions between two blocks operating in multitasking mode can go undetected. You cannot use invalid rate transitions for embedded real-time software applications.
	Single task rate transition	A rate transition between two blocks operating in single-tasking mode can go undetected. You cannot use single-tasking rate transitions for embedded real-time software applications.
	Multitask conditionally executed subsystems	A conditionally executed multirate subsystem, operating in multitasking mode, might go undetected and corrupt data or show unexpected behavior in a target system that allows preemption.
	Tasks with equal priority	Two asynchronous tasks with equal priority might go undetected and show unexpected behavior in target systems that allow preemption.
	Enforce sample times specified by Signal Specification blocks	Inconsistent sample times for a Signal Specification block and the connected destination block might go undetected and result in unpredictable execution rates.
Rationale	A	Support generation of robust and unambiguous code.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for sample time”	

ID: Title	hisl_0044: Configuration Parameters > Diagnostics > Sample Time
References	<ul style="list-style-type: none">• IEC 61508-3, Table A.3 (3) 'Language subset'• ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets'• DO-178B, Section 6.3.1b 'High-level requirements are accurate and consistent'• DO-178B, Section 6.3.2b 'Low-level requirements are accurate and consistent'• DO-178B, Section 6.3.3b 'Software architecture is consistent'
See Also	"Diagnostics Pane: Sample Time" in the Simulink documentation
Last Changed	R2012a

hisl_0301: Configuration Parameters > Diagnostics > Compatibility

ID: Title	hisl_0301: Configuration Parameters > Diagnostics > Compatibility	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Compatibility section to:	
	Compile-Time	S—function upgrades needed > error
	Run-Time	Not applicable
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for compatibility”	
See Also	“Diagnostics Pane: Compatibility” in the Simulink documentation	
Last Changed	R2011b	

hisl_0302: Configuration Parameters > Diagnostics > Data Validity > Parameters

ID: Title	hisl_0302: Configuration Parameters > Diagnostics > Data Validity > Parameters	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Data Validity > Parameters section to:	
	Compile-Time	Detect downcast > error Detect precision loss > error
	Run-Time	Detect overflow > error Detect underflow > error
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for parameters”	
See Also	“Diagnostics Pane: Data Validity” in the Simulink documentation	
Last Changed	R2011b	

hisl_0303: Configuration Parameters > Diagnostics > Data Validity > Merge block

ID: Title	hisl_0303: Configuration Parameters > Diagnostics > Data Validity > Merge block	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Data Validity > Merge block section to:	
	Compile-Time	Not applicable
	Run-Time	Detect multiple driving blocks executing at the same time step > error
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
See Also	“Diagnostics Pane: Data Validity” in the Simulink documentation	
Last Changed	R2011b	

hisl_0304: Configuration Parameters > Diagnostics > Data Validity > Model Initialization

ID: Title	hisl_0304: Configuration Parameters > Diagnostics > Data Validity > Model Initialization	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Data Validity > Model Initialization section to:	
	Compile-Time	Not applicable
	Run-Time	Underspecified initialization detection > Simplified
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for model initialization”	
See Also	“Diagnostics Pane: Data Validity” in the Simulink documentation	
Last Changed	R2011b	

hisl_0305: Configuration Parameters > Diagnostics > Data Validity > Debugging

ID: Title	hisl_0305: Configuration Parameters > Diagnostics > Data Validity > Debugging	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Data Validity > Debugging section to:	
	Compile-Time	Model Verification block enabling > Disable All
	Run-Time	Not applicable
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
See Also	“Diagnostics Pane: Data Validity” in the Simulink documentation	
Last Changed	R2011b	

hisl_0306: Configuration Parameters > Diagnostics > Connectivity > Signals

ID: Title	hisl_0306: Configuration Parameters > Diagnostics > Connectivity > Signals	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Connectivity > Signals section to:	
	Compile-Time	Not applicable
	Run-Time	Signal label mismatch > error Unconnected block input ports > error Unconnected block output ports > error Unconnected line > error
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for signal connectivity”	
See Also	“Diagnostics Pane: Connectivity” in the Simulink documentation	
Last Changed	R2011b	

hisl_0307: Configuration Parameters > Diagnostics > Connectivity > Buses

ID: Title	hisl_0307: Configuration Parameters > Diagnostics > Connectivity > Buses	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Connectivity > Buses section to:	
	Compile-Time	Not applicable
	Run-Time	Unspecified bus object at root Outputport block > error Element name mismatch > error Mux blocks used to create bus signals > error Non-bus signals treated as bus signals > error Repair bus selection > Warn and repair
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for bus connectivity”	
See Also	“Diagnostics Pane: Connectivity” in the Simulink documentation	
Last Changed	R2011b	

hisl_0308: Configuration Parameters > Diagnostics > Connectivity > Function calls

ID: Title	hisl_0308: Configuration Parameters > Diagnostics > Connectivity > Function calls	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Connectivity > Function calls section to:	
	Compile-Time	Invalid function-call connection > error
	Run-Time	Context—dependent inputs > Enable all
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings that apply to function-call connectivity”	
See Also	“Diagnostics Pane: Connectivity” in the Simulink documentation	
Last Changed	R2011b	

hisl_0309: Configuration Parameters > Diagnostics > Type Conversion

ID: Title	hisl_0309: Configuration Parameters > Diagnostics > Type Conversion	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Type Conversion section to:	
	Compile-Time	Vector / matrix block input conversion > error
	Run-Time	Not applicable
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for type conversions”	
See Also	“Diagnostics Pane: Type Conversion” in the Simulink documentation	
Last Changed	R2011b	

hisl_0310: Configuration Parameters > Diagnostics > Model Referencing

ID: Title	hisl_0310: Configuration Parameters > Diagnostics > Model Referencing	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Model Referencing section to:	
	Compile-Time	Model block version mismatch > error Port and parameter mismatch > error Invalid root Inport / Outport block connection > error Unsupported data logging > error
	Run-Time	Not applicable
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for model referencing”	
See Also	“Diagnostics Pane: Model Referencing” in the Simulink documentation	
Last Changed	R2011b	

hisl_0311: Configuration Parameters > Diagnostics > Stateflow

ID: Title	hisl_0311: Configuration Parameters > Diagnostics > Stateflow	
Description	For models used to develop high-integrity systems, in the Configuration Parameters dialog box, on the Diagnostics pane, set the parameters of the Stateflow section to:	
	Compile-Time	Unexpected backtracking > error Invalid input data access in chart initialization > error No unconditional default transitions > error Transitions outside natural parent > error Transition shadowing > error
	Run-Time	Not applicable
Note	There are two categories of diagnostics — compile-time and run-time. Prior to a simulation, compile-time diagnostics run once. During a simulation, run-time diagnostics are active at every time step. Because run-time diagnostics are active during a simulation, they impact the simulation speed. For simulations outside of a verification and validation context, consider disabling run-time diagnostics.	
Rationale	Improve robustness of design.	
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related diagnostic settings for model referencing”	
See Also	“Diagnostics Pane: Stateflow” in the Simulink documentation	
Last Changed	R2011b	

Optimizations

In this section...

“hisl_0045: Configuration Parameters > Optimization > Implement logic signals as Boolean data (vs. double)” on page 3-25

“hisl_0046: Configuration Parameters > Optimization > Block reduction” on page 3-26

“hisl_0047: Configuration Parameters > Optimization > Conditional input branch execution” on page 3-27

“hisl_0048: Configuration Parameters > Optimization > Application lifespan (days)” on page 3-28

“hisl_0051: Configuration Parameters > Optimization > Signals and Parameters > Loop unrolling threshold” on page 3-29

“hisl_0052: Configuration Parameters > Optimization > Data initialization” on page 3-30

“hisl_0053: Configuration Parameters > Optimization > Remove code from floating-point to integer conversions that wraps out-of-range values” on page 3-31

“hisl_0054: Configuration Parameters > Optimization > Remove code that protects against division arithmetic exceptions” on page 3-32

“hisl_0055: Prioritization of code generation objectives for high-integrity systems” on page 3-33

hisl_0045: Configuration Parameters > Optimization > Implement logic signals as Boolean data (vs. double)

ID: Title	hisl_0045: Configuration Parameters > Optimization > Implement logic signals as Boolean data (vs. double)	
Description	To support unambiguous behavior when using logical operators, relational operators, and the Combinatorial Logic block,	
	A	Select Implement logic signals as Boolean data (vs. double) in the Optimization pane of the Configuration Parameters dialog box.
Notes	Selecting the Implement logic signals as Boolean data (vs. double) parameter, enables Boolean type checking, which produces an error when blocks that prefer Boolean inputs connect to double signals. This checking results in generating code that requires less memory.	
Rationale	A	Avoid ambiguous model behavior and optimize memory for generated code.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related optimization settings”	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (2) 'Strongly typed programming language' • ISO/DIS 26262-6, Table 1 (c) 'Enforcement of strong typing' • DO-178B, 6.3.1e 'High-level requirements conform to standards' • DO-178B, 6.3.2e 'Low-level requirements conform to standards' • MISRA-C:2004, Rule 12.6 	
Last Changed	R2011a	

hisl_0046: Configuration Parameters > Optimization > Block reduction

ID: Title	hisl_0046: Configuration Parameters > Optimization > Block reduction	
Description	To support unambiguous presentation of the generated code and support traceability between a model and generated code,	
	A	Clear the Block reduction parameter on the Optimization pane of the Configuration Parameters dialog box.
Notes	Selecting Block reduction might optimize blocks out of the code generated for a model. This results in requirements without associated code and violates traceability objectives.	
Rationale	A	Support unambiguous presentation of generated code.
	A	Support traceability between a model and generated code.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related optimization settings”	
References	<ul style="list-style-type: none"> • IEC 61508-3, Clauses 7.4.7.2, 7.4.8.3, and 7.7.2.8 which require to demonstrate that no unintended functionality has been introduced • DO-178B, Section 6.3.4e ‘Source code is traceable to low-level requirements’ 	
See Also	“Block reduction” in the Simulink documentation	
Last Changed	R2010b	

hisl_0047: Configuration Parameters > Optimization > Conditional input branch execution

ID: Title	hisl_0047: Configuration Parameters > Optimization > Conditional input branch execution	
Description	To facilitate structural testing, in the Configuration Parameters dialog box, on the Optimization pane,	
	A	Consider clearing the Conditional input branch execution parameter.
Note	The Model Coverage tool in the Simulink Verification and Validation™ product does not account for this optimization. This optimization can result in reporting 100% coverage, but for the same test cases, code coverage might be less than 100%.	
Rationale	A	Facilitate structural testing.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related optimization settings”	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.4 (6) 'Structure-based testing' • DO-178B, Section 6.4.4.2 'Structural Coverage Analysis: Test coverage of software structure is achieved' 	
See Also	“Conditional input branch execution” in the Simulink documentation	
Last Changed	R2010b	

hisl_0048: Configuration Parameters > Optimization > Application lifespan (days)

ID: Title	hisl_0048: Configuration Parameters > Optimization > Application lifespan (days)	
Description	To support the robustness of systems that run continuously, in the Configuration Parameters dialog box, on the Optimization pane:	
	A	Set Application lifespan (days) to inf.
Notes	Embedded applications might run continuously. Do not assume a limited lifespan for timers and counters. . When you set Application lifespan (days) to inf, the simulation time is less than the application lifespan.	
Rationale	A	Support robustness of systems that run continuously.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related optimization settings”	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.4 (3) 'Defensive Programming' • ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' • DO-178B, Section 6.3.1g 'Algorithms are accurate' • DO-178B, Section 6.3.2g 'Algorithms are accurate' 	
See Also	<ul style="list-style-type: none"> • “Application lifespan (days)” in the Simulink documentation • “hisl_0040: Configuration Parameters > Solver > Simulation time” on page 3-3 	
Last Changed	R2011a	

hisl_0051: Configuration Parameters > Optimization > Signals and Parameters > Loop unrolling threshold

ID: Title	hisl_0051: Configuration Parameters > Optimization > Signals and Parameters > Loop unrolling threshold	
Description	To support unambiguous code, set the minimum signal or parameter width for generating a for loop. In the Configuration Parameters dialog box, on the Optimization > Signals and Parameters pane,	
	A	Set Loop unrolling threshold to 2 or greater.
	B	If Pack Boolean data into bitfields is selected, set Bitfield declarator type specifier to <code>uint_T</code> .
Notes	The Loop unrolling threshold parameter specifies the array size at which the code generator begins to use a for loop, instead of separate assignment statements, to assign values to the elements of a signal or parameter array. The default value is 5.	
Rationale	A	Support unambiguous generated code.
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language Subset' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • MISRA-C:2004, Rule 6.4 	
See Also	"Loop unrolling threshold" in the Simulink documentation	
Last Changed	R2012a	

hisl_0052: Configuration Parameters > Optimization > Data initialization

ID: Title	hisl_0052: Configuration Parameters > Optimization > Data initialization	
Description	To support complete definition of data and initialize internal and external data to zero, in the Configuration Parameters dialog box, on the Optimization pane,	
	A	Clear Remove root level I/O zero initialization .
	B	Clear Remove internal data zero initialization .
Note	Explicitly initialize all variables. If the run-time environment of the target system provides mechanisms to initialize all I/O and state variables, consider using the initialization of the target as an alternative to the suggested settings.	
Rationale	A, B	Support fully defined data in generated code.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related optimization settings”	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.4 (3) 'Defensive Programming' • ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' • MISRA-C:2004, Rule 9.1 	
See Also	Information about the following parameters in the Simulink documentation: <ul style="list-style-type: none"> • “Remove root level I/O zero initialization” • “Remove internal data zero initialization” 	
Last Changed	R2011a	

hisl_0053: Configuration Parameters > Optimization > Remove code from floating-point to integer conversions that wraps out-of-range values

ID: Title	hisl_0053: Configuration Parameters > Optimization > Remove code from floating-point to integer conversions that wraps out-of-range values	
Description	To support verifiable code, In the Configuration Parameters dialog box, on the Optimization pane,	
	A	Consider selecting Remove code from floating-point to integer conversions that wraps out-of-range values .
Notes	Avoid overflows as opposed to handling them with wrapper code. For blocks that have the parameter Saturate on overflow cleared, clearing Remove code from floating-point to integer conversions that wraps out-of-range values might add code that wraps out of range values, resulting in unreachable code that cannot be tested.	
Rationale	A	Support generation of code that can be verified.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related optimization settings”	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.4 (3) 'Defensive Programming' • ISO/DIS 26262-6, Table 1 (d) 'Use of defensive implementation techniques' • MISRA-C:2004, Rule 14.1 	
See Also	“Remove code from floating-point to integer conversions that wraps out-of-range values” in the Simulink documentation	
Last Changed	R2011a	

hisl_0054: Configuration Parameters > Optimization > Remove code that protects against division arithmetic exceptions

ID: Title	hisl_0054: Configuration Parameters > Optimization > Remove code that protects against division arithmetic exceptions	
Description	To support the robustness of the operations, in the Configuration Parameters dialog box, on the Optimization pane,	
	A	Clear Remove code that protects against division arithmetic exceptions .
Note	Avoid division-by-zero exceptions. If you clear Remove code that protects against division arithmetic exceptions , the code generator produces code that guards against division by zero for fixed-point data.	
Rationale	A	Protect against divide-by-zero exceptions for fixed-point code.
Model Advisor Checks	By Task > Modeling Standards for DO-178B > “Check safety-related optimization settings”	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language Subset' <li style="padding-left: 20px;">IEC 61508-3 Table A.4 (3) 'Defensive Programming' • ISO/DIS 26262-6, Table 1(b) 'Use of language subsets' <li style="padding-left: 20px;">ISO/DIS 26262-6, Table 1(d) 'Use of defensive implementation techniques' • MISRA-C:2004, Rule 21.1 	
See Also	“Remove code that protects against division arithmetic exceptions” in the Simulink documentation	
Last Changed	R2011a	

hisl_0055: Prioritization of code generation objectives for high-integrity systems

ID: Title	hisl_0055: Prioritized configuration objectives for high-integrity systems	
Description	Prioritize objectives for high-integrity systems using the Code Generation Advisor by:	
	A	Assigning the highest priority to the safety precaution objectives (Safety Precaution and Traceability)
	B	Configuring the Code Generation Advisor to run before generating code by setting Check model before generating code to On (proceed with warnings) or On (stop for warnings).
Notes	<p>Model configuration parameters provide control over many aspects of generated code. The prioritization of objectives specifies how configuration parameters are set when conflicts between objectives occur.</p> <p>Including the ROM, RAM, and Execution efficiency objectives with a lower priority in the list enables efficiency optimizations that do not conflict with Safety precautions and Traceability in the active configuration.</p> <p>Review the resulting parameter configurations to verify that safety requirements are met.</p>	
Rationale	A, B	When you use the Code Generation Advisor, configuration parameters conform to the objectives that you want and they are consistently enforced.
References	<ul style="list-style-type: none"> • DO-178B, Section 6.3.4e 'Source code is traceable to low-level requirements' • IEC61508–3, Table A.3 (3) 'Language Subset' IEC 61508–3, Table A.4 (3) 'Defensive Programming' • ISO/DIS 26262–6, Table 1(b) 'Use of language subsets' ISO/DIS 26262–6, Table 1(d) 'Use of defensive implementation techniques' 	

ID: Title	hisl_0055: Prioritized configuration objectives for high-integrity systems
See also	<ul style="list-style-type: none">• “Set Objectives — Code Generation Advisor Dialog Box”• “Manage a Configuration Set”• “cgsl_0301: Prioritization of code generation objectives for code efficiency”
Last Changed	R2011a

Stateflow Chart Considerations

- “Chart Properties” on page 4-2
- “Chart Architecture” on page 4-10

Chart Properties

In this section...
“hisf_0001: Mealy and Moore semantics” on page 4-3
“hisf_0002: User-specified state/transition execution order” on page 4-5
“hisf_0009: Strong data typing (Simulink and Stateflow boundary)” on page 4-7
“hisf_0011: Stateflow debugging settings” on page 4-8

hisf_0001: Mealy and Moore semantics

ID: Title	hisf_0001: Mealy and Moore semantics	
Description	To create Stateflow charts that implement a subset of Stateflow semantics,	
	A	In the Chart properties dialog box, set State Machine Type to Mealy.
	B	Apply consistent settings to the Stateflow charts in a model.
Note	<p>Setting State Machine Type restricts the Stateflow semantics to pure Mealy or Moore semantics. Mealy and Moore charts might be easier to understand and use in high-integrity applications.</p> <p>In Mealy charts, actions are associated with transitions. In the Moore charts, actions are associated with states.</p> <p>At compile time, the Stateflow software verifies that the chart semantics comply with the formal definitions and rules of the selected type of state machine. If the chart semantics are not in compliance, the software provides a diagnostic message.</p>	
Rationale	A, B	Promote a clear modeling style.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for DO-178B > “Check state machine type of Stateflow charts” • By Task > Modeling Standards for IEC 61508 > “Check state machine type of Stateflow charts” • By Task > Modeling Standards for ISO 26262 > “Check state machine type of Stateflow charts” 	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.7 (2) 'Simulation/modeling' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • DO-178B, Section 6.3.1b 'High-level requirements are accurate and consistent' • DO-178B, Section 6.3.1e 'High-level requirements conform to standards' • DO-178B, Section 6.3.2b 'Low-level requirements are accurate and consistent' • DO-178B, Section 6.3.2e 'Low-level requirements conform to standards' • DO-178B, Section 6.3.3b 'Software architecture is consistent' • DO-178B, Section 6.3.3e 'Software architecture conform to standards' 	

ID: Title	hisf_0001: Mealy and Moore semantics
See Also	“Building Mealy and Moore Charts” in the Stateflow documentation
Last Changed	R2011a

hisf_0002: User-specified state/transition execution order

ID: Title	hisf_0002: User-specified state/transition execution order	
Description	Do the following to explicitly set the execution order for active states and valid transitions in Stateflow charts:	
	A	In the Chart Properties dialog box, select User specified state/transition execution order .
	B	In the Stateflow Editor View menu, select Show Transition Execution Order .
	C	Set default transition to evaluate last.
Note	<p>Selecting User specified state/transition execution order restricts the dependency of a Stateflow chart semantics on the geometric position of parallel states and transitions.</p> <p>Specifying the execution order of states and transitions allows you to enforce determinism in the search order for active states and valid transitions. You have control of the order in which parallel states are executed and transitions originating from a source are tested for execution. If you do not explicitly set the execution order, the Stateflow software determines the execution order following a deterministic algorithm.</p> <p>Selecting Show Transition Execution Order displays the transition testing order.</p>	
Rationale	A, B, C	Promote an unambiguous modeling style.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for DO-178B > “Check Stateflow charts for ordering of states and transitions” • By Task > Modeling Standards for IEC 61508 > “Check usage of Stateflow constructs” • By Task > Modeling Standards for ISO 26262 > “Check usage of Stateflow constructs” 	

ID: Title	hisf_0002: User-specified state/transition execution order
References	This guideline supports adhering to: <ul style="list-style-type: none">• IEC 61508-3, Table A.3 (3) 'Language subset'• ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' ISO/DIS 26262-6, Table 1 (f) 'Use of unambiguous graphical representation'• DO-178B, Section 6.3.3b 'Software architecture is consistent' DO-178B, Section 6.3.3e 'Software architecture conform to standards '
See Also	The following topics in the Stateflow documentation <ul style="list-style-type: none">• "Transition Testing Order in Multilevel State Hierarchy"• "Execution Order for Parallel States"
Last Changed	R2011a

hisf_0009: Strong data typing (Simulink and Stateflow boundary)

ID: Title	hisf_0009: Strong data typing (Simulink and Stateflow boundary)	
Description	To support strong data typing between Simulink and Stateflow ,	
	A	Select Use Strong Data Typing with Simulink I/O .
Notes	<p>By default, input to and output from Stateflow charts are of type double. To interface directly with Simulink signals of data types other than double, select Use Strong Data Typing with Simulink I/O. In this mode, data types between the Simulink and Stateflow boundary are strongly typed, and the Simulink software does not treat the data types as double. The Stateflow chart accepts input signals of any data type supported by the Simulink software, provided that the type of the input signal matches the type of the corresponding Stateflow input data object. Otherwise, the software reports a type mismatch error.</p>	
Rationale	A	Support strongly typed code.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for IEC 61508 > “Check usage of Stateflow constructs” • By Task > Modeling Standards for ISO 26262 > “Check usage of Stateflow constructs” 	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (2) ‘Strongly typed programming language’ • ISO/DIS 26262-6, Table 1 (c) ‘Enforcement of strong typing’ • DO-178B, Section 6.3.1b ‘High-level requirements are accurate and consistent’ • DO-178B, Section 6.3.1e ‘High-level requirements conform to standards’ • DO-178B, Section 6.3.1g ‘Algorithms are accurate’ • DO-178B, Section 6.3.2b ‘Low-level requirements are accurate and consistent’ • DO-178B, Section 6.3.2e ‘Low-level requirements conform to standards’ • DO-178B, Section 6.3.2g ‘Algorithms are accurate’ • MISRA-C:2004, Rules 10.1, 10.2, 10.3 and 10.4 	
Last Changed	R2011a	

hisf_0011: Stateflow debugging settings

ID: Title	hisf_0011: Stateflow debugging settings	
Description	To protect against unreachable code and indeterminate execution time,	
	A	Select the following run-time diagnostics: <ul style="list-style-type: none"> • In the Configuration Parameters dialog box, on the Simulation Target pane, select: <ul style="list-style-type: none"> Enable debugging/animation Enable overflow detection (with debugging) • In the Stateflow Debugging window, select <ul style="list-style-type: none"> State Inconsistency Transition Conflict Detect Cycles Data Range
	B	For each truth table in the model, in the Settings menu of the Truth Table Editor, set the following parameters to Error: <ul style="list-style-type: none"> Underspecified Overspecified
Notes	Run-time diagnostics are only triggered during simulation. If the error condition is not reached during simulation, the error message is not triggered for code generation.	
Rationale	A, B	Protect against unreachable code and unpredictable execution time.
Model Advisor Checks	<ul style="list-style-type: none"> • By Task > Modeling Standards for DO-178B > “Check Stateflow debugging settings” • By Task > Modeling Standards for IEC 61508 > “Check usage of Stateflow constructs” • By Task > Modeling Standards for ISO 26262 > “Check usage of Stateflow constructs” 	

ID: Title	hisf_0011: Stateflow debugging settings
References	<ul style="list-style-type: none">• IEC 61508-3, Table A.7 (2) 'Simulation/modeling'• ISO/DIS 26262 Table 1 (d) 'Use of defensive implementation techniques'• DO-178B, Section 6.3.1b 'High-level requirements are accurate and consistent'• DO-178B, Section 6.3.1e 'High-level requirements conform to standards'• DO-178B, Section 6.3.2b 'Low-level requirements are accurate and consistent'• DO-178B, Section 6.3.2e 'Low-level requirements conform to standards'
Last Changed	R2011a

Chart Architecture

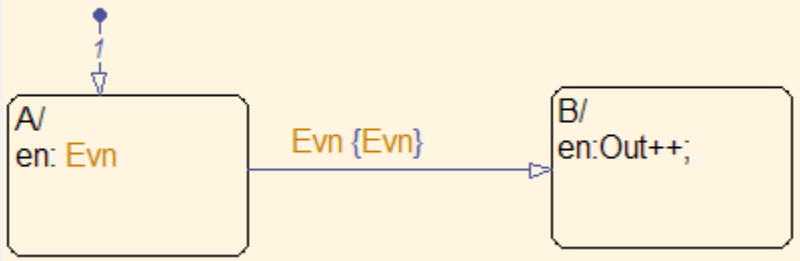
In this section...
“hisf_0003: Usage of bitwise operations” on page 4-11
“hisf_0004: Usage of recursive behavior” on page 4-12
“hisf_0007: Usage of junction conditions (maintaining mutual exclusion)” on page 4-15
“hisf_0010: Usage of transition paths (looping out of parent of source and destination objects)” on page 4-16
“hisf_0012: Chart comments” on page 4-18
“hisf_0013: Usage of transition paths (crossing parallel state boundaries)” on page 4-19
“hisf_0014: Usage of transition paths (passing through states)” on page 4-21
“hisf_0015: Strong data typing (casting variables and parameters in expressions)” on page 4-22

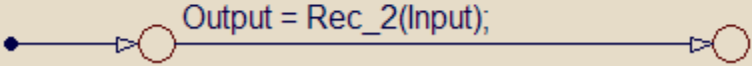
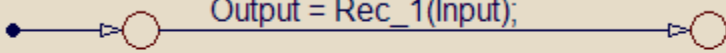
hisf_0003: Usage of bitwise operations

ID: Title	hisf_0003: Usage of bitwise operations	
Description	When using bitwise operations in Stateflow blocks,	
	A	Avoid signed integer data types as operands to the bitwise operations.
Notes	Normally, bitwise operations are not meaningful on signed integers. Undesired behavior can occur. For example, a shift operation might move the sign bit into the number, or a numeric bit into the sign bit.	
Rationale	A	Promote unambiguous modeling style.
Model Advisor Checks	By Task > Modeling Standards for MAAB > Stateflow > “Check for bitwise operations in Stateflow charts”	
References	<ul style="list-style-type: none"> • IEC 61508-3, Table A.3 (3) 'Language subset' • IEC 61508-3, Table A.3 (2) 'Strongly typed programming language' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' • ISO/DIS 26262-6, Table 1 (c) 'Enforcement of strong typing' • DO-178B, Section 6.3.1b 'High-level requirements are accurate and consistent' • DO-178B, Section 6.3.1e 'High-level requirements conform to standards' • DO-178B, Section 6.3.1g 'Algorithms are accurate' • DO-178B, Section 6.3.2b 'Low-level requirements are accurate and consistent' • DO-178B, Section 6.3.2e 'Low-level requirements conform to standards' • DO-178B, Section 6.3.2g 'Algorithms are accurate' • MISRA-C:2004, Rule 12.7 'Bitwise operators shall not be applied to operands whose underlying type is signed' 	
See Also	“hisl_0019: Usage of Bitwise Operator block”	
Last Changed	R2011a	

hisf_0004: Usage of recursive behavior

ID: Title	hisf_0004: Usage of recursive behavior	
Description	To support bounded function call behavior, avoid using design patterns that include unbounded recursive behavior. Recursive behavior is bound if you do the following:	
	A	Use an explicit termination condition that is local to the recursive call.
	B	Make sure the termination condition is reached.
Notes	This rule only applies if a chart is a classic Stateflow chart. If “hisf_0001: Mealy and Moore semantics” on page 4-3 is followed, recursive behavior is prevented due to restrictions in the chart semantics. Additionally, you can detect the error during simulation by enabling the Stateflow diagnostic Detect Cycles .	
Rationale	A, B	Promote bounded function call behavior.
References	<ul style="list-style-type: none"> • IEC 61508-3, Table B.1 (6) 'Limited use of recursion' • ISO/DIS 26262-6, Table 9 (j) 'No recursions' • DO-178B, Section 6.3.1b 'High-level requirements are accurate and consistent' • DO-178B, Section 6.3.1e 'High-level requirements conform to standards' • DO-178B, Section 6.3.1g 'Algorithms are accurate' • DO-178B, Section 6.3.2b 'Low-level requirements are accurate and consistent' • DO-178B, Section 6.3.2e 'Low-level requirements conform to standards' • DO-178B, Section 6.3.2g 'Algorithms are accurate' • MISRA-C:2004, Rule 16.2 	
Last Changed	R2011a	

ID: Title	hisf_0004: Usage of recursive behavior
Examples	<p>There are multiple patterns in Stateflow that can result in unbounded recursion.</p>  <pre> stateDiagram-v2 [*] --> A state A { en: Evn } A --> B: Evn {Evn} state B { en: Out++; } </pre>
	<p>Recursive Function Calls</p> <p>When the default state A is entered, event Evn is broadcast in the entry action of A. Evn results in a recursive call of the interpretation algorithm. Since A is active, the outgoing transition of A is tested. Since the current event Evn matches the transition event (and because of the absence of condition) the condition action is executed, broadcasting Evn again. This results in a new call of the interpretation algorithm which repeats the same sequence of steps until stack overflow.</p>

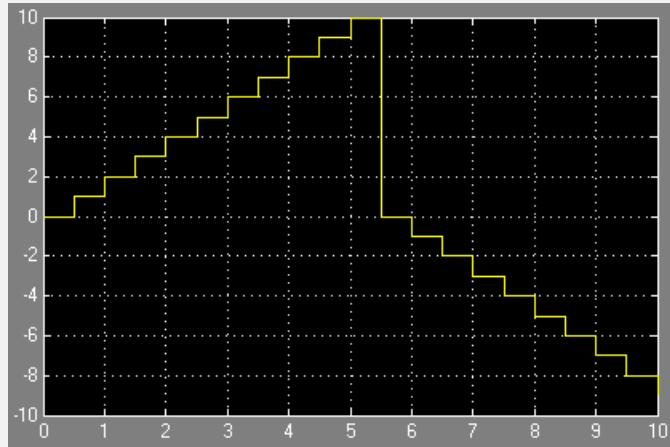
ID: Title	hisf_0004: Usage of recursive behavior
	<div data-bbox="412 361 1277 597"><p><i>function</i> Output = Rec_1(Input)</p></div> <div data-bbox="412 621 1277 874"><p><i>function</i> Output = Rec_2(Input)</p></div> <p data-bbox="397 916 700 947">Recursive Function Calls</p>

hisf_0007: Usage of junction conditions (maintaining mutual exclusion)

ID: Title	hisf_0007: Usage of junction conditions (maintaining mutual exclusion)	
Description	To enhance clarity and prevent the generation of unreachable code,	
	A	Make junction conditions mutually exclusive.
Notes	You can use this guideline to maintain a modeling language subset in high-integrity projects.	
Rationale	A	Enhance clarity and prevent generation of unreachable code.
References	<ul style="list-style-type: none"> • DO-178B, Section 6.3.1b 'High-level requirements are accurate and consistent' DO-178B, Section 6.3.1d 'High-level requirements are verifiable' DO-178B, Section 6.3.1e 'High-level requirements conform to standards' DO-178B, Section 6.3.2b 'Low-level requirements are accurate and consistent' DO-178B, Section 6.3.2d 'Low-level requirements are verifiable' DO-178B, Section 6.3.2e 'Low-level requirements conform to standards' 	
Last Changed	R2010b	

hisf_0010: Usage of transition paths (looping out of parent of source and destination objects)

ID: Title	hisf_0010: Usage of transition paths (looping out of parent of source and destination objects)		
Description	<p>Transitions that loop out of the parent of the source and destination objects are typically unintentional and cause the parent to deactivate.</p> <table border="1" data-bbox="387 505 1337 548"> <tr> <td data-bbox="387 505 468 548">A</td> <td data-bbox="473 505 1337 548">Avoid using these transitions.</td> </tr> </table>	A	Avoid using these transitions.
A	Avoid using these transitions.		
Notes	You can use this guideline to maintain a modeling language subset in high-integrity projects.		
Rationale	<table border="1" data-bbox="387 631 1337 673"> <tr> <td data-bbox="387 631 468 673">A</td> <td data-bbox="473 631 1337 673">Promote a clear modeling style.</td> </tr> </table>	A	Promote a clear modeling style.
A	Promote a clear modeling style.		
References	<ul style="list-style-type: none"> • DO-178B, Section 6.3.1b 'High-level requirements are accurate and consistent' DO-178B, Section 6.3.1e 'High-level requirements conform to standards' DO-178B, Section 6.3.1g 'Algorithms are accurate' DO-178B, Section 6.3.2b 'Low-level requirements are accurate and consistent' DO-178B, Section 6.3.2e 'Low-level requirements conform to standards' DO-178B, Section 6.3.2g 'Algorithms are accurate' 		
Last Changed	R2010b		
Examples	<p>The diagram illustrates a Stateflow chart with a parent state <code>A_Parent</code> and two sub-states, <code>A_sub_1</code> and <code>A_sub_2</code>. The parent state <code>A_Parent</code> has an entry condition <code>en: Out = 0;</code>. Sub-state <code>A_sub_1</code> has a do-action <code>du: Out++;</code> and sub-state <code>A_sub_2</code> has a do-action <code>du: Out--;</code>. A transition path loops from <code>A_sub_1</code> back to <code>A_sub_2</code> with the guard <code>[Out >= 10]</code>. The diagram is set against a yellow background.</p>		

ID: Title**hisf_0010: Usage of transition paths (looping out of parent of source and destination objects)**

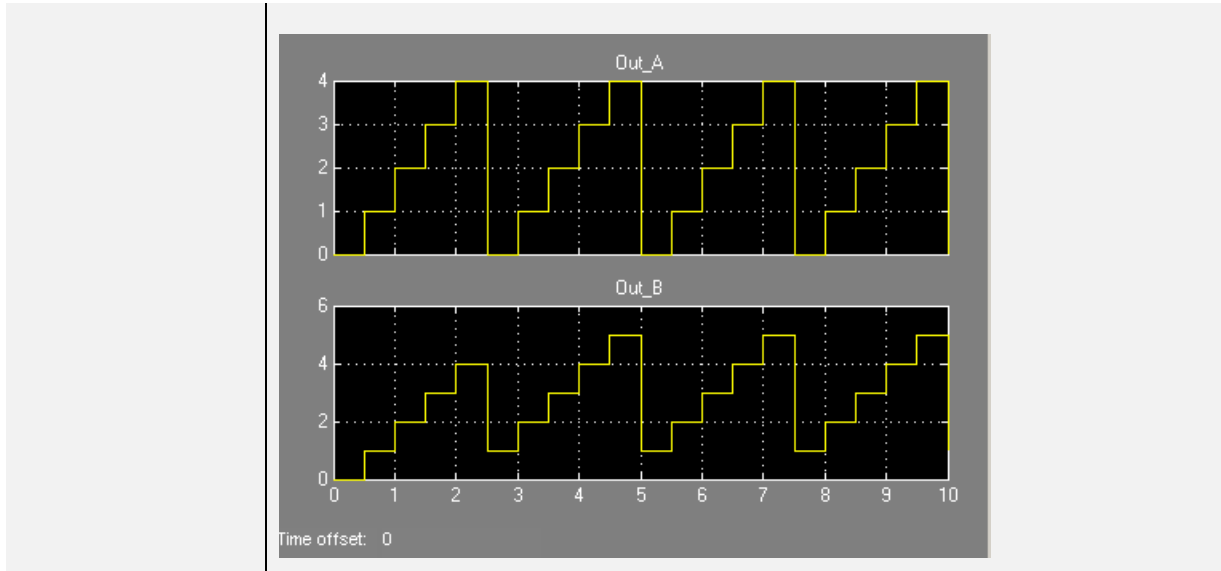
hisf_0012: Chart comments

ID: Title	hisf_0012: Chart comments	
Description	To enhance traceability between generated code and a model,	
	A	Add comments to the following Stateflow objects: In R2008b and higher: <ul style="list-style-type: none"> • Transitions In R2008a and lower: <ul style="list-style-type: none"> • Transitions • States
Notes	You can use this guideline to maintain a modeling language subset in high-integrity projects.	
Rationale	A	Enhance traceability between generated code and the corresponding model.
References	<ul style="list-style-type: none"> • DO-178B, Section 6.3.4e 'Source code is traceable to low-level requirements' 	
Last Changed	R2010b	

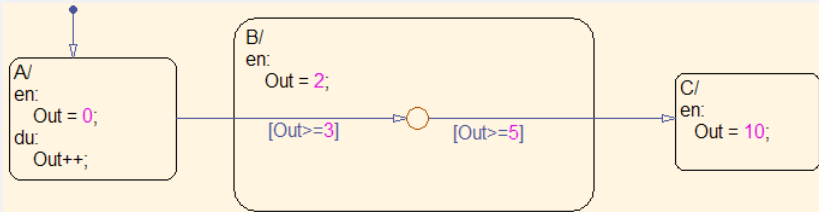
hisf_0013: Usage of transition paths (crossing parallel state boundaries)

ID: Title	hisf_0013: Usage of transition paths (crossing parallel state boundaries)	
Description	To avoid creating diagrams that are hard to understand,	
	A	Avoid creating transitions that cross from one parallel state to another.
Notes	You can use this guideline to maintain a modeling language subset in high-integrity projects.	
Rationale	A	Enhance model readability.
Last Changed	R2010b	
Example	<p>In the following example, when Out_A is 4, both parent states (A_Parent and B_Parent) are reentered. Reentering the parent states resets the values of Out_A and Out_B to zero.</p> <pre> stateDiagram-v2 state A_Parent_1 { [*] --> A_sub_1 state A_sub_1 { du: Out_A++; } A_sub_1 --> A_sub_2: [Out_A==5] 2 state A_sub_2 { du: Out_A--; } } state B_Parent_2 { [*] --> B_sub_1 state B_sub_1 { du: Out_B++; } B_sub_1 --> B_sub_2: [Out_B==7] state B_sub_2 { du: Out_B--; } } A_Parent_1 --> B_Parent_2: [Out_A==4] 1 B_Parent_2 --> A_Parent_1: [Out_A==4] 1 </pre>	

ID: Title	hisf_0013: Usage of transition paths (crossing parallel state boundaries)
------------------	--

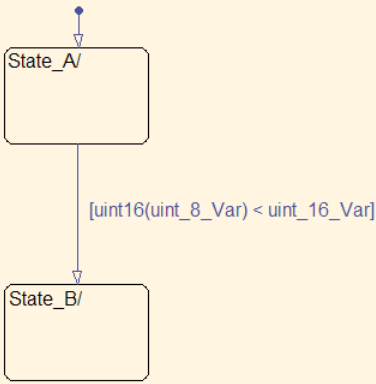
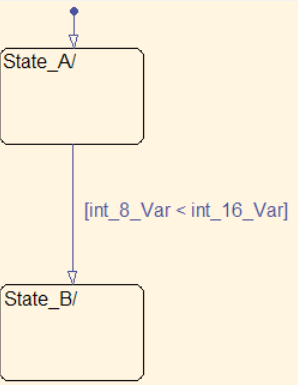


hisf_0014: Usage of transition paths (passing through states)

ID: Title	hisf_0014: Usage of transition paths (passing through states)	
Description	To avoid creating diagrams that are confusing and include transition paths without benefit,	
	A	Avoid transition paths that go into and out of a state without ending on a substate.
Notes	You can use this guideline to maintain a modeling language subset in high-integrity projects.	
Rationale	A	Enhance model readability.
References	<ul style="list-style-type: none"> • DO-178B, Section 6.3.1b 'High-level requirements are accurate and consistent' DO-178B, Section 6.3.1e 'High-level requirements conform to standards' DO-178B, Section 6.3.2b 'Low-level requirements are accurate and consistent' DO-178B, Section 6.3.2e 'Low-level requirements conform to standards' 	
Last Changed	R2010b	
Examples	 <pre> stateDiagram-v2 [*] --> A state A { en: Out = 0; du: Out++; } A --> B state B { en: Out = 2; } B --> B B --> C state C { en: Out = 10; } C --> B </pre>	

hisf_0015: Strong data typing (casting variables and parameters in expressions)

ID: Title	hisf_0015: Strong data typing (casting variables and parameters in expressions)	
Description	To facilitate strong data typing.	
	A	Explicitly type cast variables and parameters of different data types in: <ul style="list-style-type: none"> • Transition evaluations • Transition assignments • Assignments in states
Notes	The Stateflow software automatically casts variables of different type into the same data type. This guideline helps clarify data types of the intermediate variables.	
Rationale	A	Apply strong data typing.
References	<ul style="list-style-type: none"> • DO-178B, Section 6.3.1b 'High-level requirements are accurate and consistent' DO-178B, Section 6.3.1e 'High-level requirements conform to standards' DO-178B, Section 6.3.1g 'Algorithms are accurate' DO-178B, Section 6.3.2b 'Low-level requirements are accurate and consistent' DO-178B, Section 6.3.2e 'Low-level requirements conform to standards' DO-178B, Section 6.3.2g 'Algorithms are accurate' 	

ID: Title	hisf_0015: Strong data typing (casting variables and parameters in expressions)
Last Changed	R2010b
Examples	 <p data-bbox="397 829 583 857">Recommended</p>  <p data-bbox="397 1298 639 1326">Not Recommended</p>

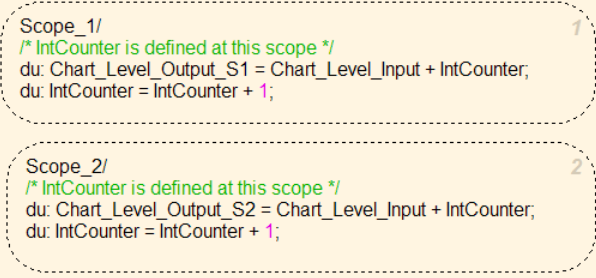
MISRA-C:2004 Compliance Considerations

- “Modeling Style” on page 5-2
- “Block Usage” on page 5-14
- “Configuration Settings” on page 5-19
- “Stateflow Chart Considerations” on page 5-24
- “System Level” on page 5-35

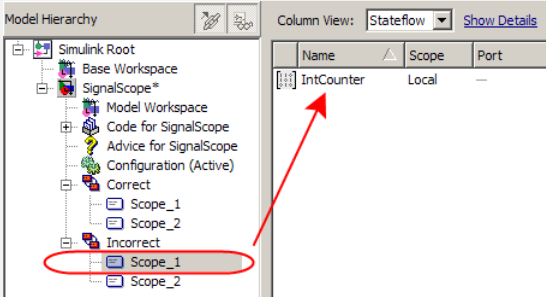
Modeling Style

In this section...
“hisl_0061: Unique identifiers for clarity” on page 5-3
“hisl_0062: Global variables in graphical functions” on page 5-5
“hisl_0063: Length of user-defined function names to improve MISRA-C:2004 compliance” on page 5-8
“hisl_0064: Length of user-defined type object names to improve MISRA-C:2004 compliance” on page 5-9
“hisl_0065: Length of signal and parameter names to improve MISRA-C:2004 compliance” on page 5-10
“hisl_0201: Define reserved keywords to improve MISRA-C:2004 compliance” on page 5-11
“hisl_0202: Use of data conversion blocks to improve MISRA-C:2004 compliance” on page 5-12

hisl_0061: Unique identifiers for clarity

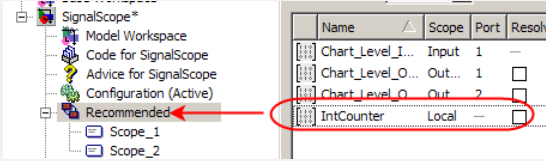
ID: Title	hisl_0061: Unique identifiers for clarity	
Description	When developing a model,	
	A	Use unique identifiers for Simulink signals.
	B	Define unique identifiers across multiple scopes within a chart.
Notes	The code generator automatically resolves conflicts between identifiers so that symbols in the generated code are unique. The process is called name mangling.	
Rationale	A, B	Improve readability of a graphical model and mapping between identifiers in the model and generated code.
References	<ul style="list-style-type: none"> • MISRA-C: 2004 5.6 • DO-178B, Section 6.3.2b 'Accuracy and Consistency of Low-Level Requirements' • IEC 61508–3, Table A.3 (3) 'Language subset' IEC 61508–3, Table A.4 (5) 'Design and coding standards' • ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' ISO/DIS 26262-6, Table 1 (e) 'Use of established design principles' ISO/DIS 26262-6, Table 1 (h) 'Use of naming conventions' 	
See Also	"Construction of Symbols" in the Simulink Coder™ documentation	
Last Changed	R2011a	
Examples	In the following example, two states use identifier <i>IntCounter</i> .	
	 <pre> Scope_1/ /* IntCounter is defined at this scope */ du: Chart_Level_Output_S1 = Chart_Level_Input + IntCounter; du: IntCounter = IntCounter + 1; Scope_2/ /* IntCounter is defined at this scope */ du: Chart_Level_Output_S2 = Chart_Level_Input + IntCounter; du: IntCounter = IntCounter + 1; </pre> <p>The identifier <i>IntCounter</i> is defined for two states, <i>Scope_1</i> and <i>Scope_2</i>.</p>	

ID: Title **hisl_0061: Unique identifiers for clarity**



Not Recommended

To clarify the model, create unique identifiers—for example, *IntCounter_S1* and *IntCounter_S2*—or define *IntCounter* at the parent level.

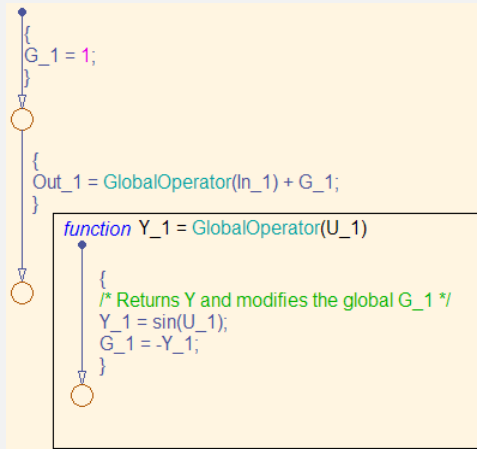


Recommended

The images show the Simulink Model Hierarchy and Column View. In the 'Not Recommended' case, the 'IntCounter' block is located within a child scope (Scope_1), and its name is not unique. In the 'Recommended' case, the 'IntCounter' block is located at the parent level, and its name is unique. Red circles and arrows highlight the relevant elements in both screenshots.

hisl_0062: Global variables in graphical functions

ID: Title	hisl_0062: Global variables in graphical functions								
Description	For data with a global scope used in a function								
	A	Do not use the data in the calling expression if a value is assigned to the data in that function.							
Rationale	A	Enhance readability of a model by removing ambiguity in the values of global variables.							
References	<ul style="list-style-type: none"> IEC 61508–3, Table A.3 (3) 'Language subset' IEC 61508–3, Table A.4 (4) 'Modular approach' IEC 61508–3, A.4 (5) 'Design and coding standards' ISO/DIS 26262-6, Table 1 (b) 'Use of language subsets' ISO/DIS 26262-6, Table 1 (f) 'Use of unambiguous graphical representation' ISO/DIS 26262-6, Table 1 (h) 'Use of naming conventions' DO-178B, Section 6.3.4f 'Accuracy and Consistency of Source Code' MISRA-C: 2004 12.2 MISRA-C: 2004 12.4 								
Last Changed	R2011a								
Examples	<p>The basic expression is</p> $Y = f(U) + G$ <p>where in the function G is assigned a value. This modeling pattern is realized:</p>								
	<table border="1"> <thead> <tr> <th>In a...</th> <th>By Using...</th> </tr> </thead> <tbody> <tr> <td>Model</td> <td>Data stores</td> </tr> <tr> <td>Stateflow chart</td> <td>Functions</td> </tr> <tr> <td>MATLAB code</td> <td>Subfunctions</td> </tr> </tbody> </table> <p>In the following example, the function GlobalOperator overwrites the initial value of G_1,</p>		In a...	By Using...	Model	Data stores	Stateflow chart	Functions	MATLAB code
In a...	By Using...								
Model	Data stores								
Stateflow chart	Functions								
MATLAB code	Subfunctions								

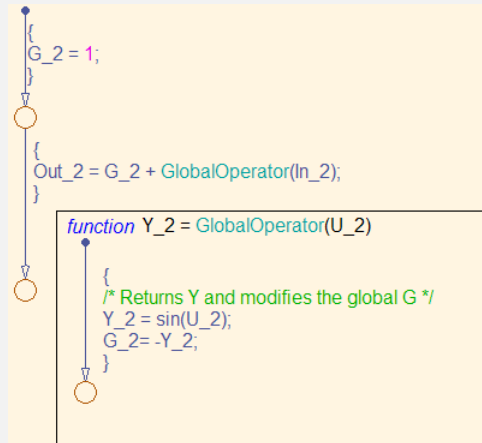


```

static real_T GlobalOperator_1(real_T U_1)
{
    real_T Y_1;

    /* Returns Y and modifies the global G_1 */
    Y_1 = sin(U_1);
    DWork.G_1 = -Y_1;
    return Y_1;
}
  
```

In the next example, the function uses the initial value of 1 for global variable G_2 before the chart tries to assign the variable another value. The generated code omits the assignment of G_2 to negative Y_2 . (If the chart uses G_2 at a later point, the chart uses the updated value of negative Y_2 .)



```

static real_T GlobalOperator_2(real_T U_2)
{
  real_T Y_2;

  /* Returns Y and modifies the global G */
  Y_2 = sin(U_2);
  DWork.G_2 = -Y_2;
  return Y_2;
}

```

Code generator behavior is consistent and predictable.

hisl_0063: Length of user-defined function names to improve MISRA-C:2004 compliance

ID: Title	hisl_0063: Length of user-defined function names to improve MISRA-C:2004 compliance	
Description	To improve MISRA-C:2004 compliance of the generated code when working with Subsystem blocks with the block parameter Function name options set to <code>User specified</code> :	
	A	Limit the length of data object names to 31 characters or fewer.
	For this rule, Subsystem blocks include standard Simulink Subsystems, MATLAB Function blocks, and Stateflow blocks.	
Rationale	A	Function names longer than 31 characters might result in a MISRA-C:2004 violation.
References	<ul style="list-style-type: none"> • MISRA-C:2004 Rule 5.1 	
Prerequisites	“hisl_0060: Configuration parameters that improve MISRA-C:2004 compliance”	
Last Changed	R2011a	

hisl_0064: Length of user-defined type object names to improve MISRA-C:2004 compliance

ID: Title	hisl_0064: Length of user-defined type object names to improve MISRA-C:2004 compliance
Description	<p>To improve MISRA-C:2004 compliance of the generated code, limit the length of data object names to 31 characters or fewer for:</p> <ul style="list-style-type: none"> • Simulink.AliasType • Simulink.NumericType • Simulink.Variant • Simulink.Bus • Simulink.BusElement • Simulink.IntEnumType
Rationale	The length of the type definitions in the generated code name might result in a MISRA-C:2004 violation.
References	<ul style="list-style-type: none"> • MISRA-C:2004 Rule 5.1
Prerequisites	“hisl_0060: Configuration parameters that improve MISRA-C:2004 compliance”
Last Changed	R2011a

hisl_0065: Length of signal and parameter names to improve MISRA-C:2004 compliance

ID: Title	hisl_0065: Length of signal and parameter names to improve MISRA-C:2004 compliance
Description	To improve MISRA-C:2004 compliance of the generated code, limit the length of signal and parameter names to 31 characters or fewer when using any of the following storage classes: <ul style="list-style-type: none"> • Exported global • Imported Extern • Imported Extern Pointer • Custom storage class
Rationale	The length of the signal and parameter name might result in a MISRA-C:2004 violation.
References	<ul style="list-style-type: none"> • MISRA-C:2004 Rule 5.1
Prerequisites	“hisl_0060: Configuration parameters that improve MISRA-C:2004 compliance”
Last Changed	R2011a

hisl_0201: Define reserved keywords to improve MISRA-C:2004 compliance

ID: Title	hisl_0201: Define reserved keywords to improve MISRA-C:2004 compliance	
Description	To improve MISRA-C: 2004 compliance of the generated code, define reserved keywords to prevent identifier clashes within the project namespace.	
	A	In the Configuration Parameters dialog box, on the Simulation Target > Symbols > Reserved names pane, define reserved identifiers.
	B	Use a consistent set of reserved identifiers for all models.
Notes	Simulink Coder checks models for standard C language key words. Expand the list of reserved identifiers to include project specific identifiers. Examples include target-specific clashes, standard and custom library clashes, and other identified clashes.	
Rationale	Improve MISRA-C:2004 compliance of the generated code.	
See Also	<ul style="list-style-type: none"> • “Simulation Target Pane: Symbols” in the Simulink documentation • “Reserved Keywords” in the Simulink Coder documentation • “Reserved names” in the Simulink Coder documentation 	
References	MISRA-C:2004, Rule 20.2	
Last Changed	R2011b	

hisl_0202: Use of data conversion blocks to improve MISRA-C:2004 compliance

ID: Title	hisl_0202: Use of data conversion blocks to improve MISRA-C:2004 compliance
Description	<p>To improve MISRA-C:2004 compliance of generated code, insert a data type conversion block when using signals of type single (<code>real132_T</code>) as inputs to the following blocks:</p> <ul style="list-style-type: none"> • Math • Trigonometry • Sqrt <p>The data type conversion block to changes the data type to double (<code>real_T</code>)</p>
Rationale	Improve MISRA-C:2004 compliance of the generated code.
Notes	The function prototypes for many math functions require an input of type double. To accommodate the function prototype, you can add a data type conversion block. As an alternative to the data type conversion block, you could define a new function interface using the Target Function Library (TFL).

ID: Title	hisl_0202: Use of data conversion blocks to improve MISRA-C:2004 compliance
References	• MISRA-C: 2004 Rule 10.2
Last Changed	R2012a

Example	<p>Not Recommended Add a data type conversion block to the input signal of the block. Convert the output signal back to single.</p>
---------	--

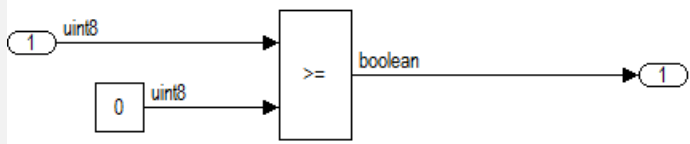
Block Usage

In this section...
“hisl_0020: Blocks not recommended for MISRA-C:2004 compliance” on page 5-14
“hisl_0101: Avoid invariant comparison operations to improve MISRA-C:2004 compliance” on page 5-15
“hisl_0102: Data type of loop control variables to improve MISRA-C:2004 compliance” on page 5-18

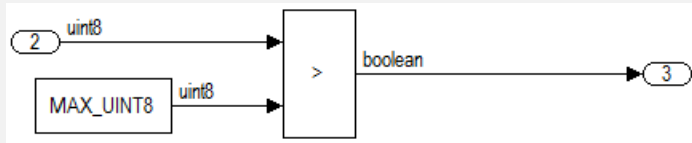
hisl_0020: Blocks not recommended for MISRA-C:2004 compliance

ID: Title	hisl_0020: Blocks not recommended for MISRA-C:2004 compliance	
Description	To improve MISRA-C:2004 compliance of the generated code,	
	A	Use only blocks that support code generation, as documented in the Simulink Block Support Table
	B	Do not use blocks that are listed as “Not recommended for production code” in the Simulink Block Support Table
Notes	<p>If you follow this and other modeling guidelines, you increase the likelihood of generating code that complies with the MISRA-C:2004 standard.</p> <p>Choose Simulink Help > Block Support Table > Simulink to view the block support table.</p> <p>Blocks with the footnote (4) in the Block Support Table are classified as “Not Recommended for production code.”</p>	
Rationale	A,B	Improve MISRA-C:2004 compliance of the generated code.
Model Advisor Checks	By Product > Embedded Coder > “Check for blocks not recommended for MISRA-C:2004 compliance”	
References	MISRA-C:2004	
Last Changed	R2011a	

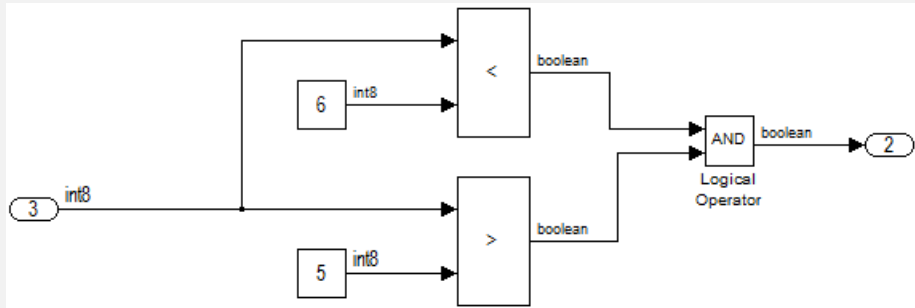
hisl_0101: Avoid invariant comparison operations to improve MISRA-C:2004 compliance

ID: Title	hisl_0101: Avoid invariant comparison operations to improve MISRA-C:2004 compliance
Description	<p>To improve MISRA-C:2004 compliance of generated code, avoid comparison operations with invariant results. Comparison operations are performed by the following blocks:</p> <ul style="list-style-type: none"> • If • Logic • Relational Operator • Switch • Switch Case • Compare to Constant
Rationale	Improve MISRA-C:2004 compliance of the generated code.
References	<ul style="list-style-type: none"> • MISRA-C: 2004 Rule 13.7 • MISRA-C: 2004 Rule 14.1
Last Changed	R2012a
Example	<p>Invariant comparisons can occur in simple or compound comparison operations. In compound comparison operations, the individual components can be variable when the full calculation is invariant.</p> <p>Simple: A uint8 is always greater then or equal to 0.</p>  <pre> graph LR A((1)) -- uint8 --> B[>=] C[0] -- uint8 --> B B -- boolean --> D((1)) </pre> <p>Simple: A uint8 cannot have a value greater then 256</p>

ID: Title	hisl_0101: Avoid invariant comparison operations to improve MISRA-C:2004 compliance
------------------	--



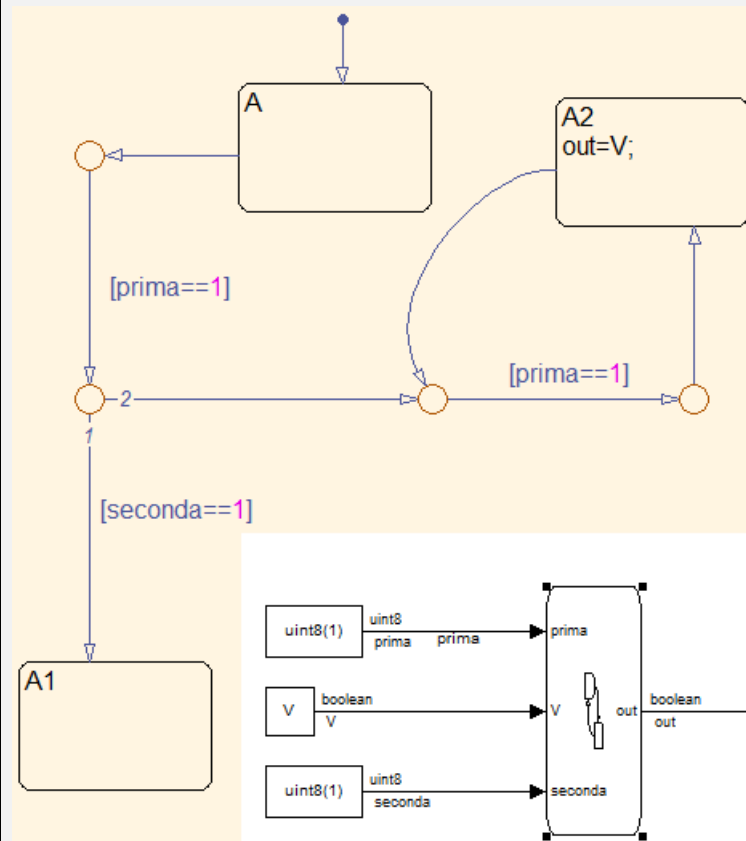
Compound: The comparison operations are mutually exclusive



Stateflow:

ID: Title

hisl_0101: Avoid invariant comparison operations to improve MISRA-C:2004 compliance



hisl_0102: Data type of loop control variables to improve MISRA-C:2004 compliance

ID: Title	hisl_0102: Data type of loop control variables to improve MISRA-C:2004 compliance
Description	<p>To improve MISRA-C:2004 compliance of generated code, use integer data type for variables that are used as loop control counter variables in:</p> <ul style="list-style-type: none"> • For and while loops constructed in Stateflow and MATLAB. • While Iterator and For Iterator blocks.
Rationale	Improve MISRA-C:2004 compliance of the generated code.
References	<ul style="list-style-type: none"> • MISRA-C: 2004 Rule 13.7
Last Changed	R2012a

Configuration Settings

In this section...
<p>“hisl_0060: Configuration parameters that improve MISRA-C:2004 compliance” on page 5-19</p> <p>“hisl_0312: Specify target specific configuration parameters to improve MISRA-C:2004 compliance” on page 5-21</p> <p>“hisl_0313: Selection of bitfield data types to improve MISRA-C:2004 compliance” on page 5-23</p>

hisl_0060: Configuration parameters that improve MISRA-C:2004 compliance

ID: Title	hisl_0060: Configuration parameters that improve MISRA-C:2004 compliance																			
Description	To improve MISRA-C:2004 compliance of the generated code,																			
	A	Set the following model configuration parameters as specified:																		
		<table border="1"> <thead> <tr> <th>Pane / Configuration Parameter</th> <th>Value</th> </tr> </thead> <tbody> <tr> <td colspan="2">Diagnostics > Data Validity</td> </tr> <tr> <td>Model Verification block enabling</td> <td>Disable All</td> </tr> <tr> <td colspan="2">Code Generation pane</td> </tr> <tr> <td>System target file</td> <td>ERT-based target</td> </tr> <tr> <td colspan="2">Code Generation > Interface pane</td> </tr> <tr> <td>Support: non-finite numbers</td> <td>Cleared (off)</td> </tr> <tr> <td>Support: continuous time</td> <td>Cleared (off)</td> </tr> <tr> <td>Support: non-inlined S-functions</td> <td>Cleared (off)</td> </tr> </tbody> </table>	Pane / Configuration Parameter	Value	Diagnostics > Data Validity		Model Verification block enabling	Disable All	Code Generation pane		System target file	ERT-based target	Code Generation > Interface pane		Support: non-finite numbers	Cleared (off)	Support: continuous time	Cleared (off)	Support: non-inlined S-functions	Cleared (off)
Pane / Configuration Parameter	Value																			
Diagnostics > Data Validity																				
Model Verification block enabling	Disable All																			
Code Generation pane																				
System target file	ERT-based target																			
Code Generation > Interface pane																				
Support: non-finite numbers	Cleared (off)																			
Support: continuous time	Cleared (off)																			
Support: non-inlined S-functions	Cleared (off)																			

ID: Title		hisl_0060: Configuration parameters that improve MISRA-C:2004 compliance	
		MAT-file logging	Cleared (off)
		Code replacement library	C89/C90 (ANSI)
		Code Generation > Code Style pane	
		Parenthesis level	Maximum (Specify precedence with parentheses)
		Code Generation > Symbols pane	
		Maximum identifier length	31
Note	If you follow this and other modeling guidelines, you increase the likelihood of generating code that complies with the MISRA-C:2004 standard.		
Rationale	A	Improve MISRA-C:2004 compliance of the generated code.	
Model Advisor Checks	By Product > Embedded Coder > “Check configuration parameters for MISRA-C:2004 compliance”		
References	<ul style="list-style-type: none"> • MISRA-C:2004 		
Last Changed	R2011a		

hisl_0312: Specify target specific configuration parameters to improve MISRA-C:2004 compliance

ID: Title	hisl_0312: Specify target specific configuration parameters to improve MISRA-C:2004 compliance	
Description	To improve MISRA-C:2004 compliance of generated code, use a consistent set of model parameters. The parameters include, but are not limited to:	
	A	Explicitly setting model character encoding using the <code>slCharacterEncoding(encoding)</code> function.
	B	In the Configuration Parameters dialog box, explicitly selecting a Hardware Implementation > Embedded hardware (simulation and code generation) > Signed integer division rounds to: parameter.
	C	If complex numbers are not required, deselecting the Code Generation > Interface > Software Environment > complex numbers parameter.
Notes	Base the selection of the integer division method on the target hardware and compiler. When available, in the Configuration Parameters dialog box, specify both of these parameters: <ul style="list-style-type: none"> • Hardware Implementation > Embedded hardware (simulation and code generation) > Device vendor • Hardware Implementation > Embedded hardware (simulation and code generation) > Device type 	
Rationale	Improve MISRA-C:2004 compliance of the generated code.	
See Also	<ul style="list-style-type: none"> • “Configure Emulation and Embedded Target Hardware” in the Simulink Coder documentation. • <code>slCharacterEncoding</code> in the Simulink documentation. • “hisl_0060: Configuration parameters that improve MISRA-C:2004 compliance” 	

ID: Title	hisl_0312: Specify target specific configuration parameters to improve MISRA-C:2004 compliance
References	<ul style="list-style-type: none">• MISRA-C: 2004 Rule 3.2• MISRA-C: 2004 Rule 3.3• MISRA-C: 2004 Rule 5.7
Last Changed	R2012a

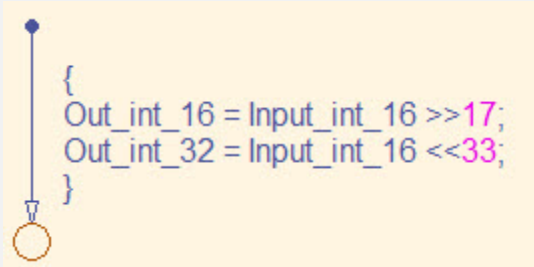
hisl_0313: Selection of bitfield data types to improve MISRA-C:2004 compliance

ID: Title	hisl_0313: Selection of bitfield data types to improve MISRA-C:2004 compliance
Description	To improve MISRA-C:2004 compliance of generated code when bitfields are used, in the Configuration Parameters dialog box, set Optimization > Signals and Parameters > Code generation > Bitfield declarator type specifier to <code>uint_T</code> .
Rationale	Improve MISRA-C:2004 compliance of the generated code.
Notes	Set Bitfield declarator type specifier to <code>uint_T</code> if any of the following Optimization parameters are enabled: <ul style="list-style-type: none"> • Optimization > Signals and Parameters > Code generation > Pack Boolean data into bitfields • Optimization > Stateflow > Code generation > Use bitsets for storing state configuration • Optimization > Stateflow > Code generation > Use bitsets for storing Boolean data
See Also	<ul style="list-style-type: none"> • “Optimization Pane: Signals and Parameters” in the Simulink documentation.
References	<ul style="list-style-type: none"> • MISRA-C: 2004 Rule 6.4
Last Changed	R2012a

Stateflow Chart Considerations

In this section...
“hisf_0064: Shift operations for Stateflow data to improve MISRA-C:2004 compliance” on page 5-25
“hisf_0065: Type cast operations in Stateflow to improve MISRA-C:2004 compliance” on page 5-27
“hisf_0211: Protect against use of unary operators in Stateflow Charts to improve MISRA-C:2004 compliance ” on page 5-29
“hisf_0212: Data type of Stateflow for loop control variables to improve MISRA-C: 2004 compliance ” on page 5-31
“hisf_0213: Protect against divide-by-zero calculations in Stateflow charts to improve MISRA-C: 2004 compliance” on page 5-32

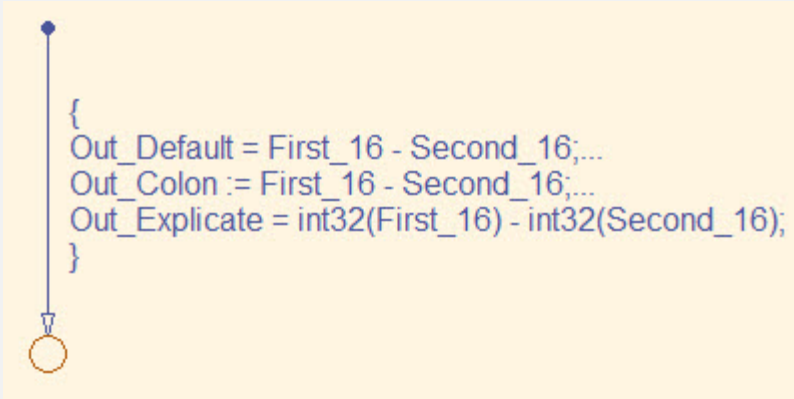
hisf_0064: Shift operations for Stateflow data to improve MISRA-C:2004 compliance

ID: Title	hisf_0064: Shift operations for Stateflow data to improve MISRA-C:2004 compliance	
Description	To improve MISRA-C:2004 compliance of the generated code with Stateflow bit-shifting operations, do not perform:	
	A	Right-shift operations greater than the bit-width of the input type
	B	Left-shift operations greater than the bit-width of the output type
Note	If you follow this and other modeling guidelines, you increase the likelihood of generating code that complies with the MISRA-C:2004 standard.	
Rationale	A,B	To avoid shift operations in the generated code that might be a MISRA-C:2004 violation.
References	<ul style="list-style-type: none"> • MISRA-C:2004 Rule 12.7 	
Prerequisites	“hisf_0060: Configuration parameters that improve MISRA-C:2004 compliance”	
Last Changed	R2011a	
Example	<p>In the first equation, shifting 17 bits to the right pushes data stored in a 16-bit word out of range. The resulting output is zero. In the second equation, shifting the data 33 bits pushes data beyond the range of storage for a 32-bit word. Again, the resulting output is zero.</p> 	

ID: Title	hisf_0064: Shift operations for Stateflow data to improve MISRA-C:2004 compliance
------------------	--

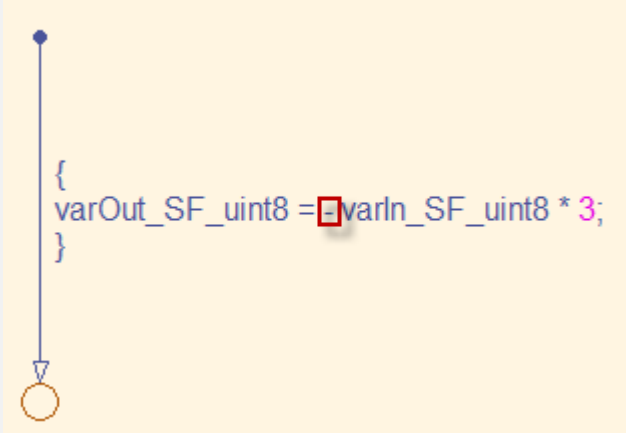
```
void stateflow_shift_passed_step(void)
{
    Out_int_16 = (int16 T) (Input_int_16 >> 17);
    Out_int_32 = Input_int_16 << 33;
}
```

hisf_0065: Type cast operations in Stateflow to improve MISRA-C:2004 compliance

ID: Title	hisf_0065: Type cast operations in Stateflow to improve MISRA-C:2004 compliance	
Description	To improve MISRA-C:2004 compliance of the generated code, protect against Stateflow casting integer and fixed-point calculations to wider data types than the input data types by:	
	A	Explicitly type casting the calculations
	B	Using the := notation in Stateflow
Note	If you follow this and other modeling guidelines, you increase the likelihood of generating code that complies with the MISRA-C:2004 standard.	
Rationale	A,B	To avoid shift operations in the generated code that might be a MISRA-C:2004 violation.
References	<ul style="list-style-type: none"> • MISRA-C:2004 Rule 10.1 • MISRA-C:2004 Rule 10.4 	
Prerequisites	"hisf_0060: Configuration parameters that improve MISRA-C:2004 compliance"	
Last Changed	R2011a	
Example	<p>The example shows the default behavior and both methods of controlling the casting (explicitly type casting and using the colon operator).</p>  <pre> { Out_Default = First_16 - Second_16;... Out_Colon := First_16 - Second_16;... Out_Explicate = int32(First_16) - int32(Second_16); } </pre>	

ID: Title	hisf_0065: Type cast operations in Stateflow to improve MISRA-C:2004 compliance
	<pre>void stateflow_wide_shift_step(void) { <u>Out_Default</u> = <u>First_16</u> - <u>Second_16</u>; <u>Out_Colon</u> = (int32_T)<u>First_16</u> - (int32_T)<u>Second_16</u>; <u>Out_Explicate</u> = (int32_T)<u>First_16</u> - (int32_T)<u>Second_16</u>; }</pre>

hisf_0211: Protect against use of unary operators in Stateflow Charts to improve MISRA-C:2004 compliance

ID: Title	hisf_0211: Protect against use of unary operators in Stateflow Charts to improve MISRA-C:2004 compliance
Description	To improve MISRA-C:2004 compliance of the generated code:
	A Do not use unary minus operators on unsigned data types
Note	The Stateflow action language does not restrict the use of unary minus operators on unsigned expressions.
Rationale	A Improve MISRA-C:2004 compliance of the generated code.
References	<ul style="list-style-type: none"> MISRA-C:2004 Rule 12.9
Last Changed	R2011b
Example	<p>Not Recommended:</p>  <pre> /* Gateway: Chart */ /* During: Chart */ /* Transition: '<S1>:1' */ varOut_SF_uint8 = (uint8_T) (-varIn_SF_uint8 * 3); </pre>

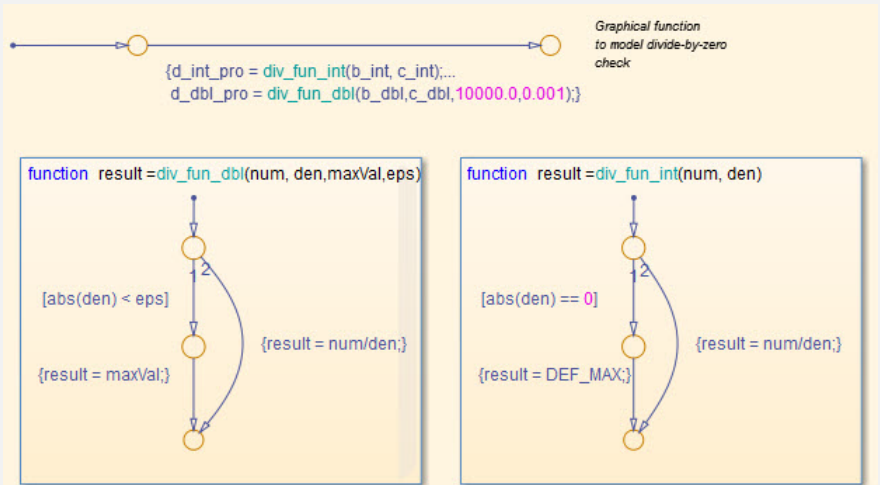
ID: Title	hisf_0211: Protect against use of unary operators in Stateflow Charts to improve MISRA-C:2004 compliance
	<p>Applying the unary minus operator to the unsigned integer results in a MISRA-C:2004 Rule 12.9 violation. The resulting output wraps around the maximum value of 256 (uint8). In this example, if the input variable In_SF_uint8 equals 7, then the output variable varOut_uint8 equals $256 - (7 * 3)$, or 235. The simulation and code generation values are in agreement.</p>

hisf_0212: Data type of Stateflow for loop control variables to improve MISRA-C: 2004 compliance

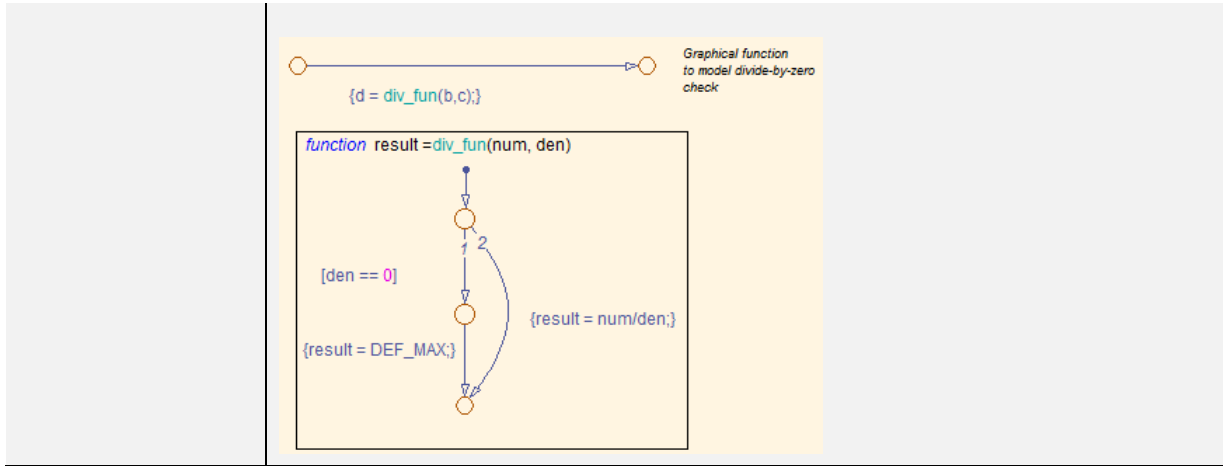
ID: Title	hisf_0212: Data type of Stateflow for loop control variables to improve MISRA-C: 2004 compliance	
Description	To improve MISRA-C:2004 compliance of the generated code:	
	A	Explicitly select an integer data type as the control variable in a Stateflow for loop
Note	The default data type in Simulink and Stateflow is double. Explicitly select an integer data type.	
Rationale	A	Improve MISRA-C:2004 compliance of the generated code
References	<ul style="list-style-type: none"> • MISRA-C:2004 Rule 13.4 	
Last Changed	R2011b	

hisf_0213: Protect against divide-by-zero calculations in Stateflow charts to improve MISRA-C: 2004 compliance

ID: Title	hisf_0213: Protect against divide-by-zero calculations in Stateflow charts to improve MISRA-C: 2004 compliance	
Description	To improve MISRA-C:2004 compliance of the generated code for floating point and integer-based operations, do one of the following:	
	A	Perform static analysis of the model to prove that division by zero is not possible
	B	Provide run-time error checking in the generated C code by explicitly modeling the error checking in Stateflow
	C	Modify the code generation process using Code Replacement Libraries (CRLs) to protect against division by zero
	D	For integer-based operations, in the Configuration Parameters dialog box, on the Optimization pane, clear Remove code that protects against division arithmetic exceptions
Note	<p>Using run-time error checking introduces additional computational and memory overhead in the generated code. It is preferable to use static analysis tools to limit errors in the generated code. You can use Simulink Design Verifier™ or Polyspace® to perform the static analysis.</p> <p>If static analysis determines that sections of the code can have a division by zero, then add run-time protection into that section of the model (see example). Using a modified CRL or selecting the parameter Remove code that protects against division arithmetic exceptions protects division operations against divide-by-zero operations. However, this action does introduce additional computational and memory overhead.</p> <p>Use only one of the run-time protections (B, C or D) in a model. Using more than one option can result in redundant protection operations.</p>	
Rationale	A,B, C,D	Improve MISRA-C:2004 compliance of the generated code
References	<ul style="list-style-type: none"> MISRA-C:2004 Rule 21.1 	

ID: Title	hisf_0213: Protect against divide-by-zero calculations in Stateflow charts to improve MISRA-C: 2004 compliance
See Also	<ul style="list-style-type: none"> • “Code Replacement” • “hisl_0002: Usage of Math Function blocks (remainder and reciprocal)” • “hisl_0005: Usage of Product blocks” • “hisl_0054: Configuration Parameters > Optimization > Remove code that protects against division arithmetic exceptions”
Last Changed	R2011b
Example	<p>Run-time divide by zero protection can be realized using a graphical function. Unique functions should be provided for each data type.</p>  <p>The diagram illustrates a graphical function for divide-by-zero protection. At the top, a horizontal line with two yellow circles represents the function signature. Below it, the code is: <code>{d_int_pro = div_fun_int(b_int, c_int);... d_dbl_pro = div_fun_dbl(b_dbl, c_dbl, 10000.0, 0.001);}</code>. To the right, the text reads: "Graphical function to model divide-by-zero check".</p> <p>Below this are two example function blocks:</p> <ul style="list-style-type: none"> Left block: <code>function result = div_fun_dbl(num, den, maxVal, eps)</code>. It shows a stateflow chart with three nodes. The first node has a guard <code>[abs(den) < eps]</code>. If true, the flow goes to the second node with <code>{result = maxVal;}</code>. If false, the flow goes to the third node with <code>{result = num/den;}</code>. Right block: <code>function result = div_fun_int(num, den)</code>. It shows a stateflow chart with three nodes. The first node has a guard <code>[abs(den) == 0]</code>. If true, the flow goes to the second node with <code>{result = DEF_MAX;}</code>. If false, the flow goes to the third node with <code>{result = num/den;}</code>.

ID: Title	hisf_0213: Protect against divide-by-zero calculations in Stateflow charts to improve MISRA-C: 2004 compliance
------------------	---



System Level

In this section...
“hisl_0401: Encapsulation of code to improve MISRA-C:2004 compliance” on page 5-35
“hisl_0402: Use of custom #pragma to improve MISRA-C:2004 compliance” on page 5-36
“hisl_0403: Use of char data type improve MISRA-C:2004 compliance” on page 5-37

hisl_0401: Encapsulation of code to improve MISRA-C:2004 compliance

ID: Title	hisl_0401: Encapsulation of code to improve MISRA-C:2004 compliance
Description	To improve the MISRA-C:2004 compliance of the generated code, encapsulate manually inserted code. This code includes, but is not limited to, C, Fortran, and assembly code.
Rationale	Improve MISRA-C:2004 compliance of the generated code
See Also	<ul style="list-style-type: none"> “External Code Integration” in the Embedded Coder documentation. “External Code Integration” in the Simulink Coder documentation.
Notes	<p>Simulink provides multiple methods for integrating existing code. The user is responsible for encapsulating the generated code.</p> <p>Encapsulation can be defined as “the process of compartmentalizing the elements of an abstraction that constitute its structure and behavior; encapsulation serves to separate the contractual interface of an abstraction and its implementation” ^a</p>
References	<ul style="list-style-type: none"> MISRA-C: 2004 Rule 2.1
Last Changed	R2012a

^aBooch, Grady, R. Maksimchuk, M. Engle, B. Young, J. Conallen, K. Houston. *Object-Oriented Analysis and Design with Applications*. 3rd ed. Boston, MA: Addison-Wesley Professional, 2007.

hisl_0402: Use of custom #pragma to improve MISRA-C:2004 compliance

ID: Title	hisl_0402: Use of custom #pragma to improve MISRA-C:2004 compliance	
Description	To improve the MISRA-C:2004 compliance of the generated code, document user defined pragma. In the documentation, include:	
	A	Memory range (start and stop address)
	B	Intended use
	C	Justification for using a pragma
Rationale	Improve MISRA-C:2004 compliance of the generated code	
See Also	<ul style="list-style-type: none"> • “Custom Storage Classes” in the Embedded Coder documentation. • “Memory Sections” in the Embedded Coder documentation. • “Document a Code Generation Project Using the Simulink Report Generator™” in the Simulink Coder documentation. 	
Notes	The Simulink Report Generator documents pragmas.	
References	<ul style="list-style-type: none"> • MISRA-C: 2004 Rule 3.4 	
Last Changed	R2012a	

hisl_0403: Use of char data type improve MISRA-C:2004 compliance

ID: Title	hisl_0403: Use of char data type to improve MISRA-C:2004 compliance	
Description	To improve the MISRA-C:2004 compliance of the generated code with custom storage classes that use the Char data type, only use:	
	A	Plain char type for character values.
	B	Signed and unsigned char type for numeric values.
Rationale	Improve MISRA-C:2004 compliance of the generated code.	
See Also	<ul style="list-style-type: none"> • “Custom Storage Classes” in the Embedded Coder documentation. • “Memory Sections” in the Embedded Coder documentation. • “Document a Code Generation Project Using the Simulink Report Generator” in the Simulink Coder documentation. 	
References	<ul style="list-style-type: none"> • MISRA-C: 2004 Rule 6.1 • MISRA-C: 2004 Rule 6.2 	
Last Changed	R2012a	